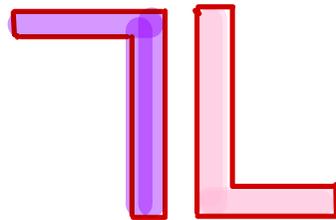


Verifiable Delay Functions

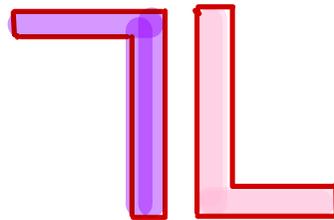
- Archisman Dutta



Verifiable Delay Functions

(or how to harness the power of time)

- Archisman Dutta



Plan for the afternoon

- Timed-release crypto
- Time-lock puzzles
- Verifiable Delay Functions
- Pietrzak's construction

Plan for the afternoon

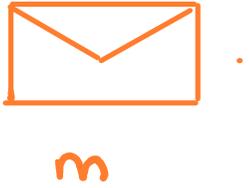
- Timed-release crypto
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Timed-release crypto

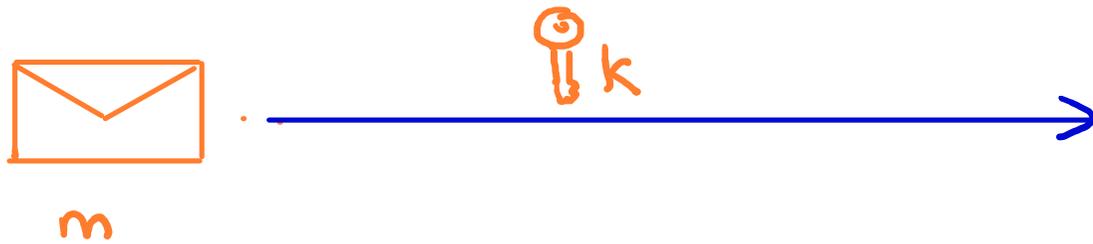
“Send information into the future”

(sort of an inverse of Steins;Gate)

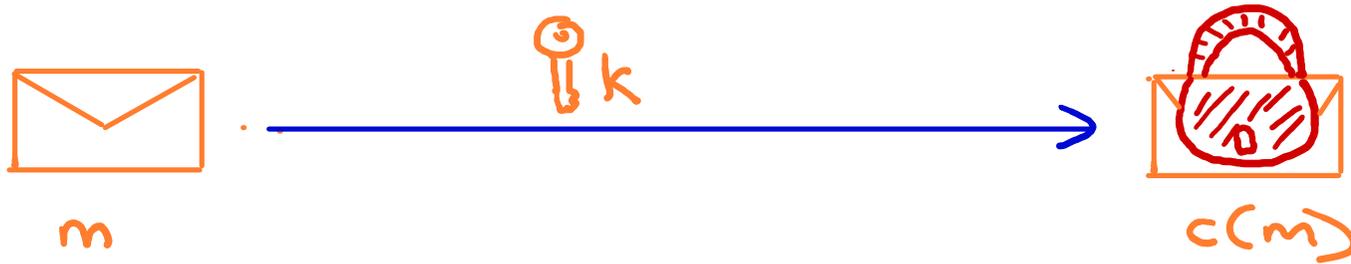
Timed-release crypto



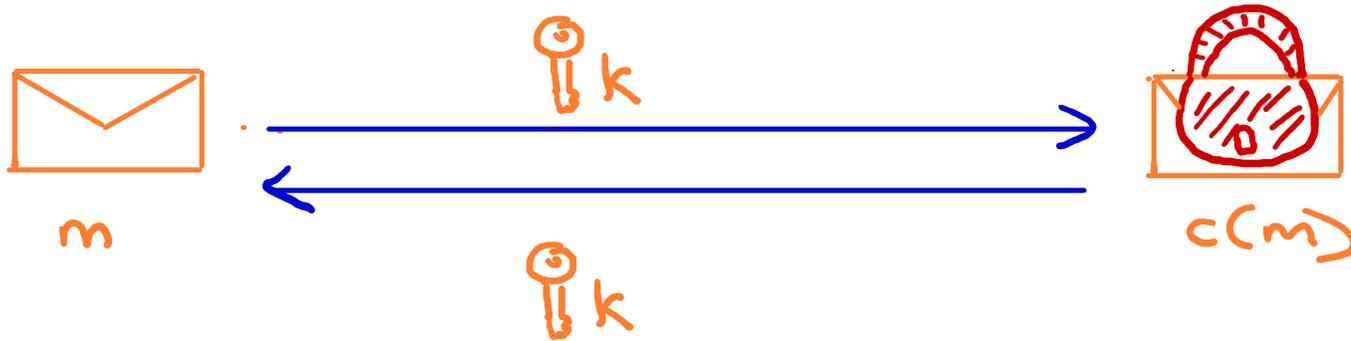
Timed-release crypto



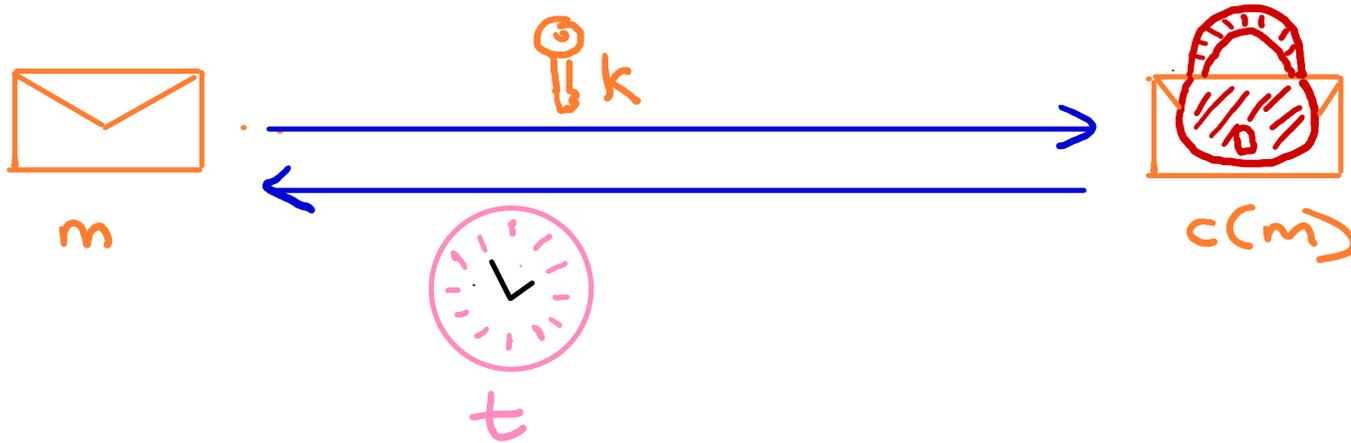
Timed-release crypto



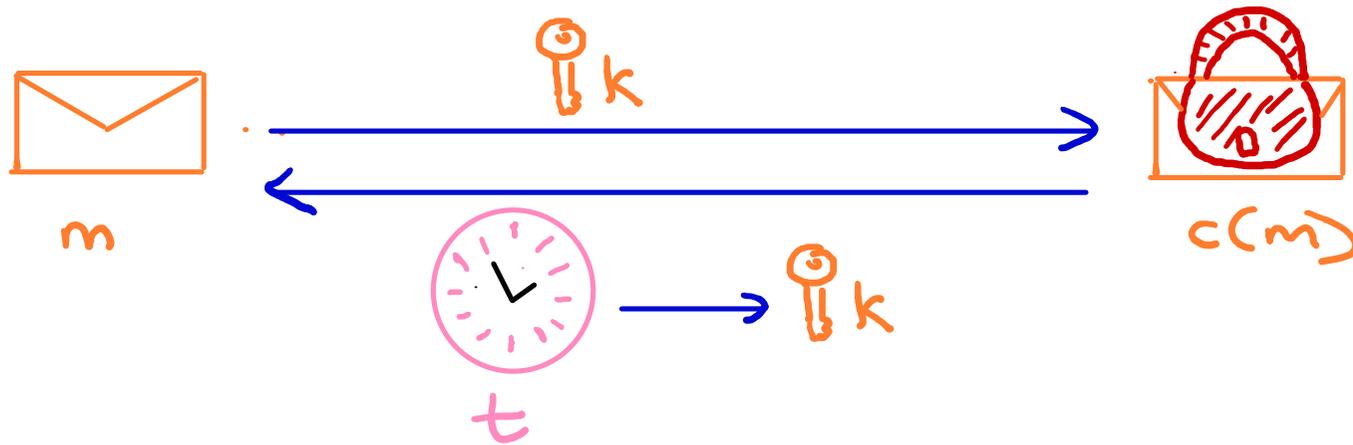
Timed-release crypto



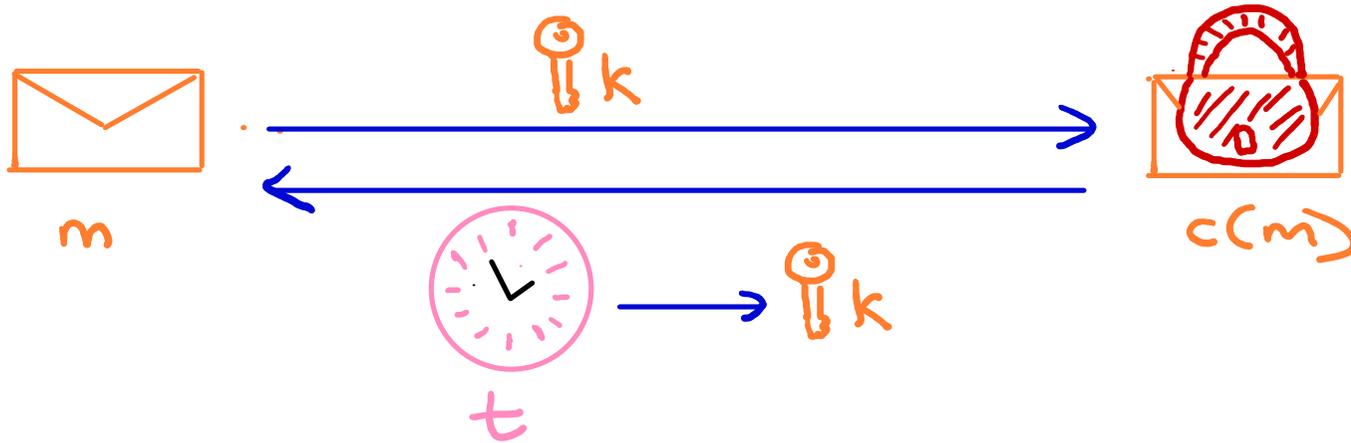
Timed-release crypto



Timed-release crypto

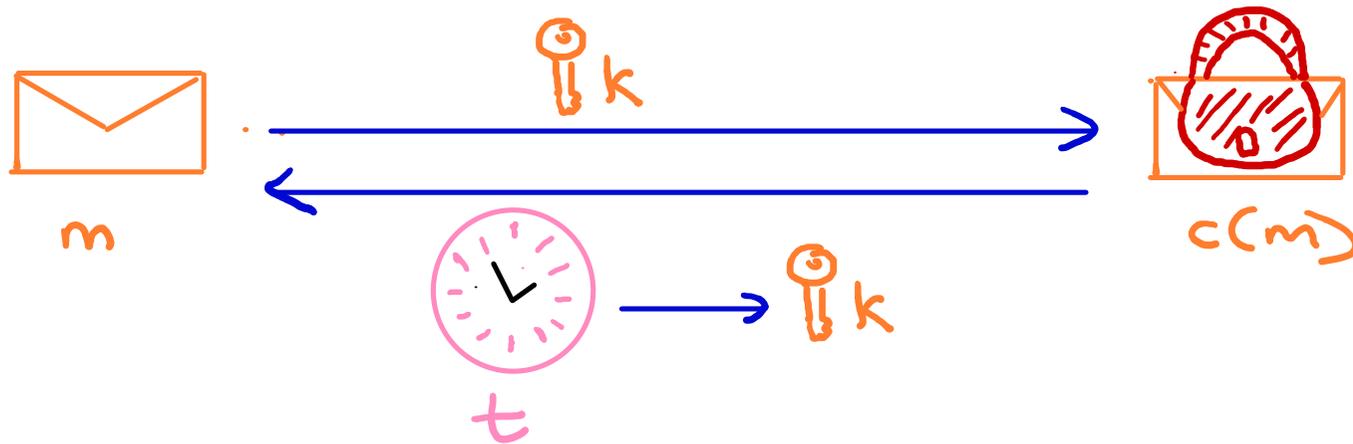


Timed-release crypto



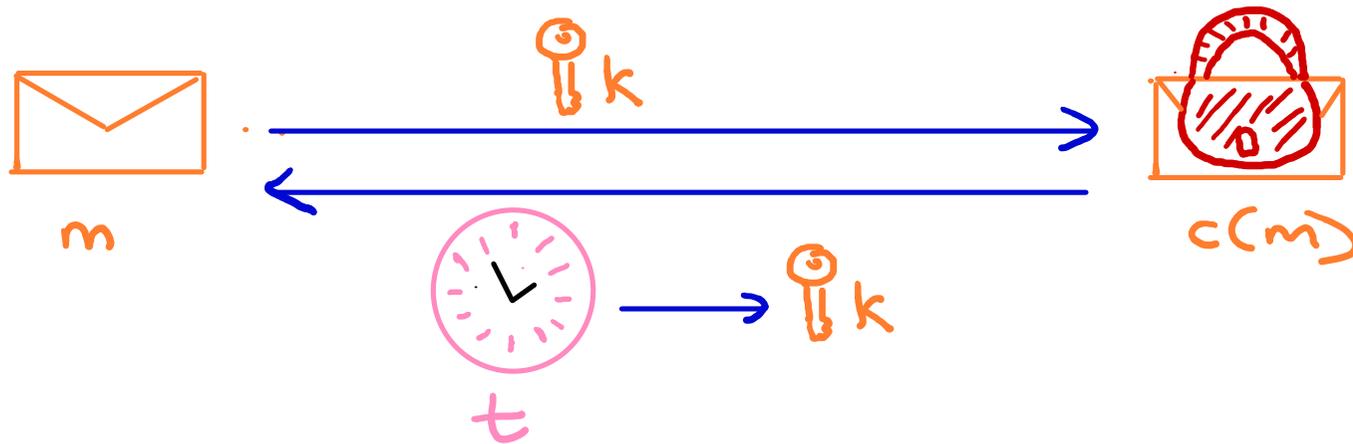
$c(m)$ cannot be decrypted without k

Timed-release crypto



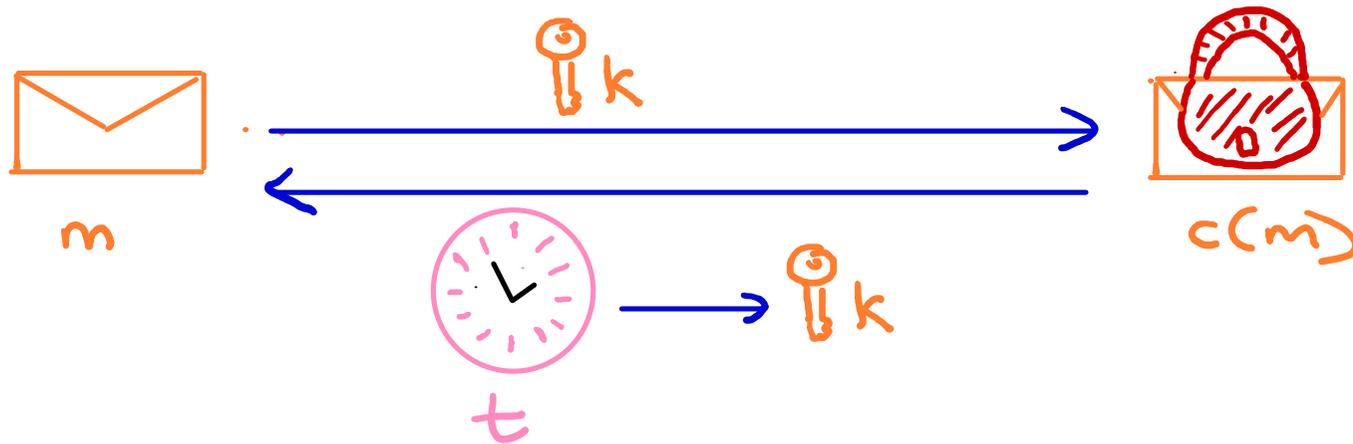
$c(m)$ cannot be decrypted without k
but k is **only** obtained after time t

Timed-release crypto



$c(m)$ cannot be decrypted without k
but k is **only** obtained after time t^*

Timed-release crypto

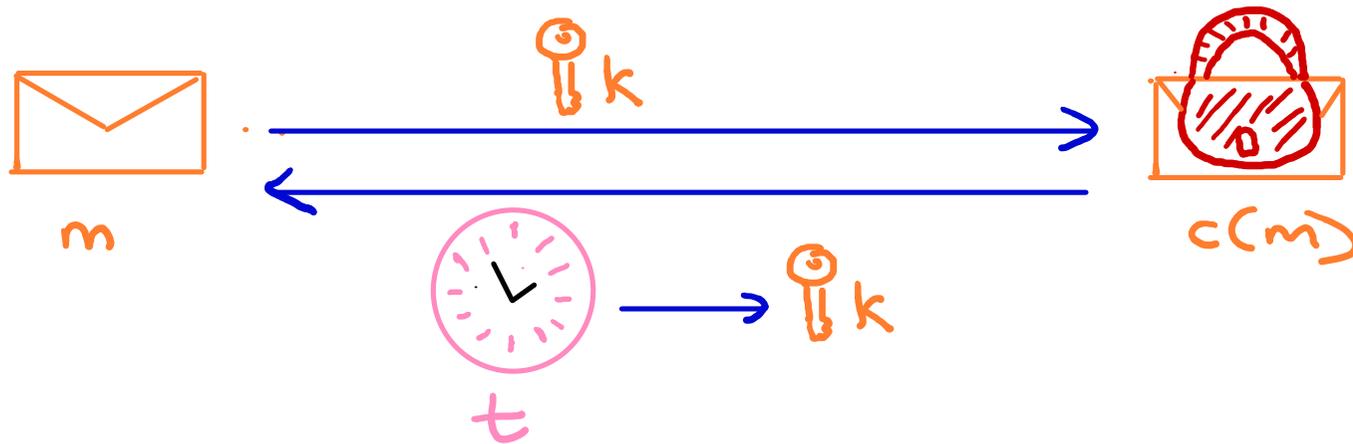


$c(m)$ cannot be decrypted without k

but k is **only** obtained after time t^*

* $t \rightarrow$ **wall-clock** time (not **CPU** time)

Timed-release crypto



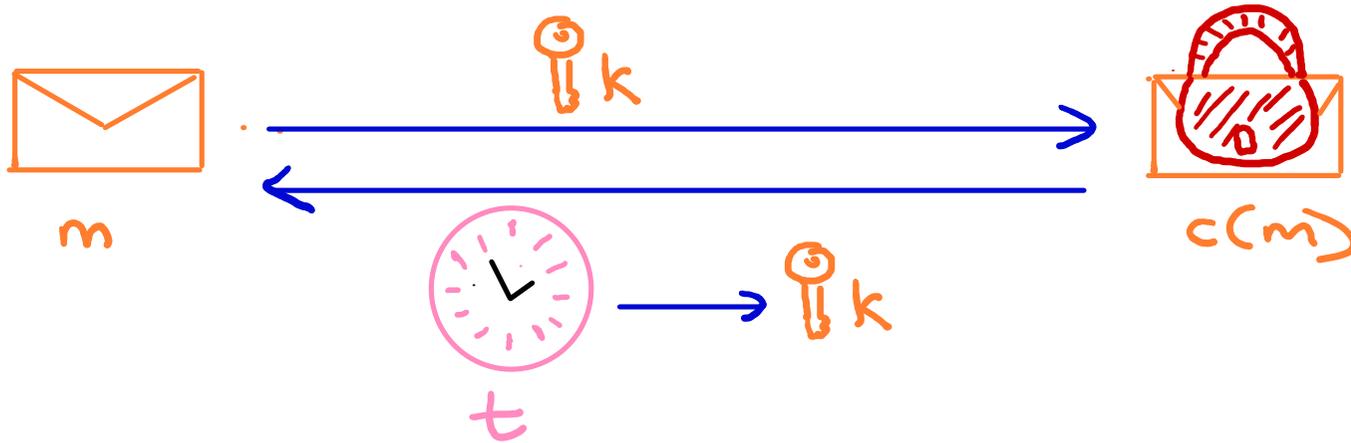
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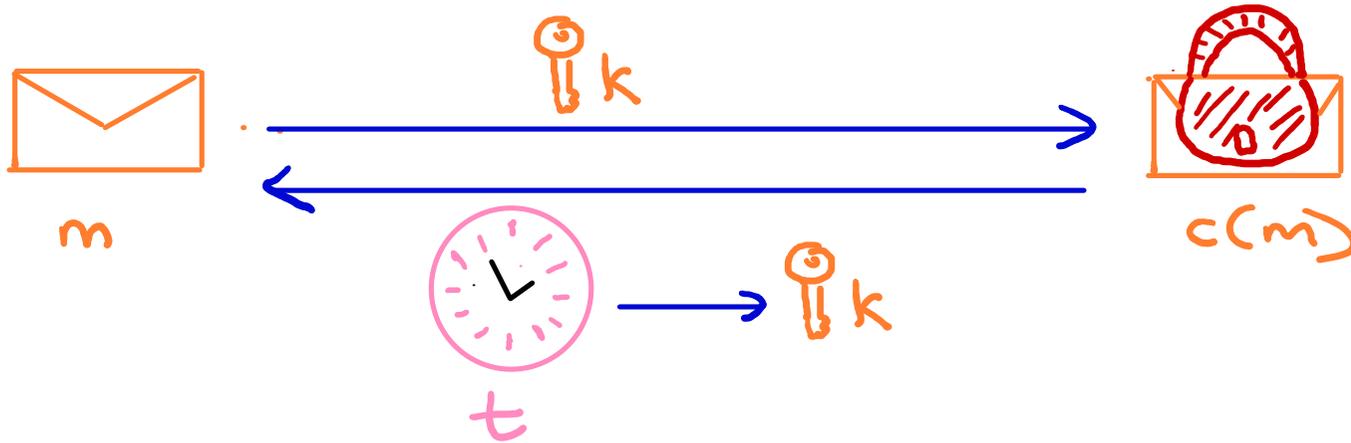
wall-clock time < **CPU** time for **parallel** processes

Timed-release crypto



Applications?

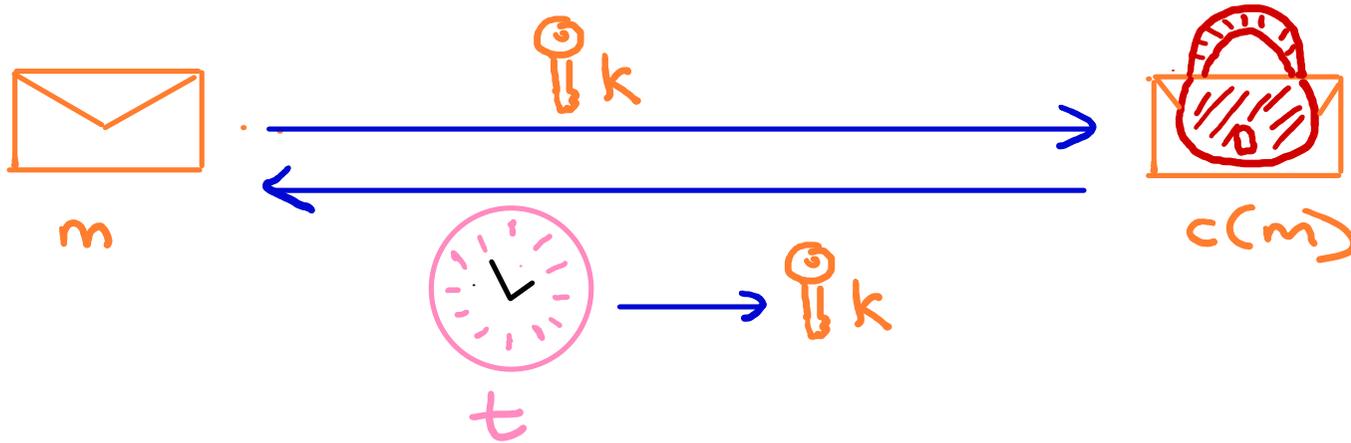
Timed-release crypto



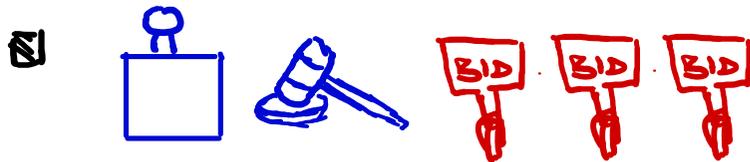
Applications?



Timed-release crypto

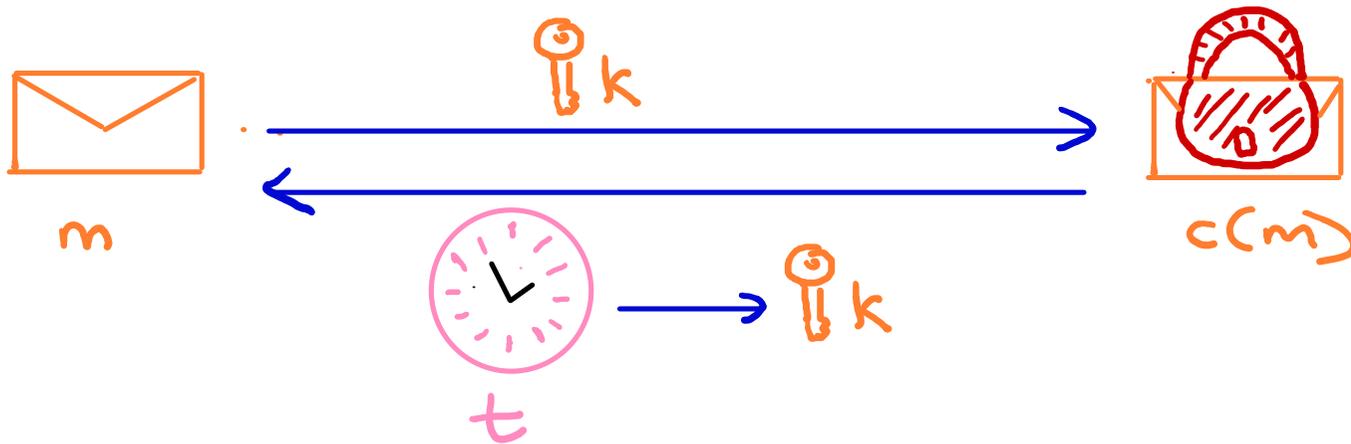


Applications?

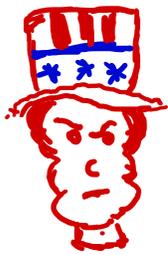


Sotheby's

Timed-release crypto

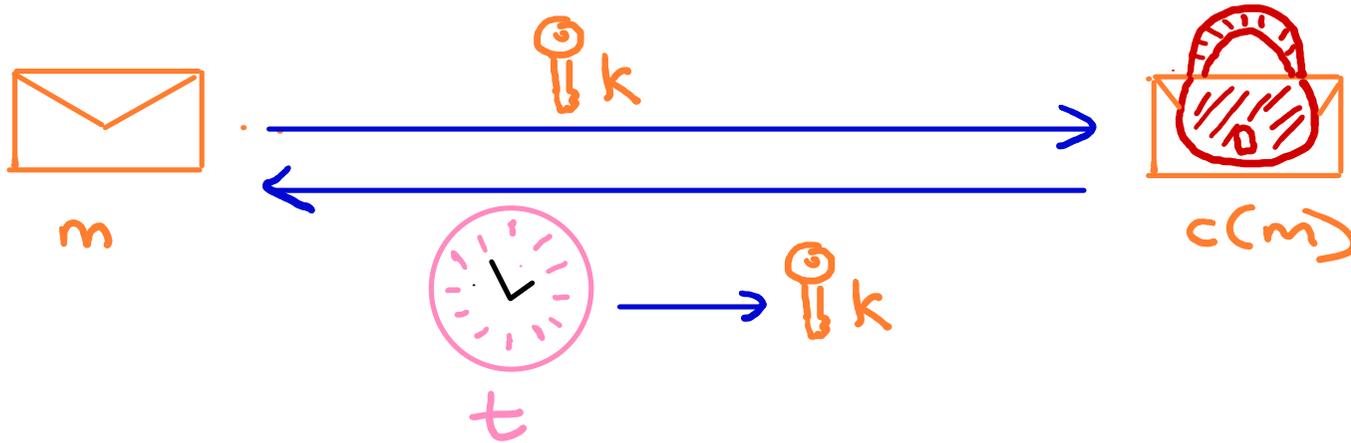


Applications?

☑  * But only after a year....

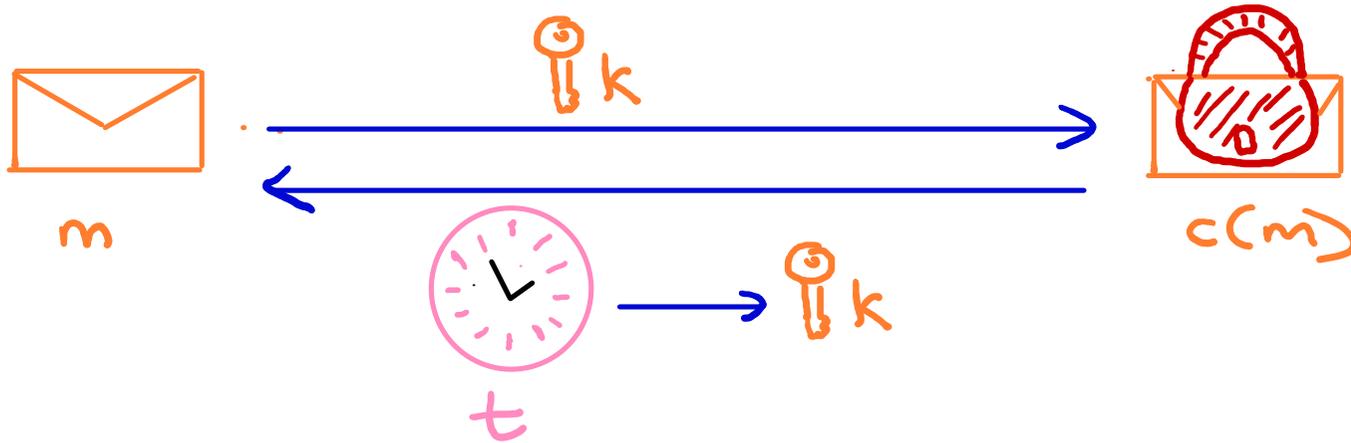
No-Spying Agency

Timed-release crypto

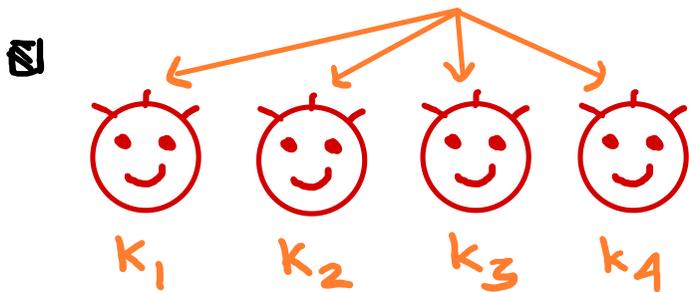


How to implement?

Timed-release crypto

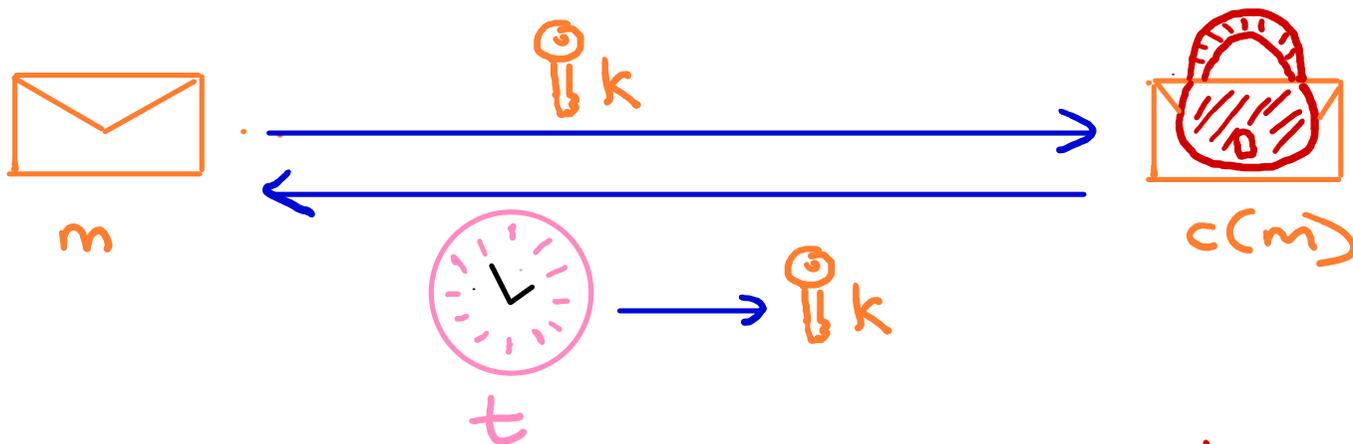


How to implement?



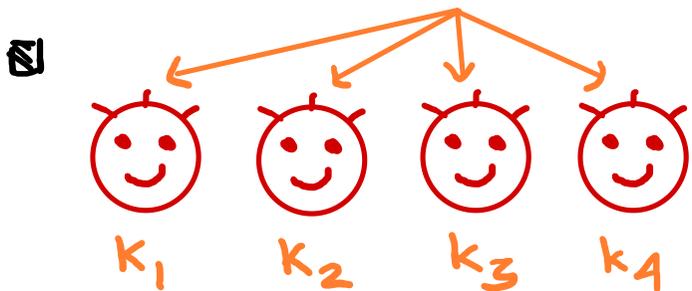
$$k = (k_1, k_2, k_3, k_4)$$

Timed-release crypto



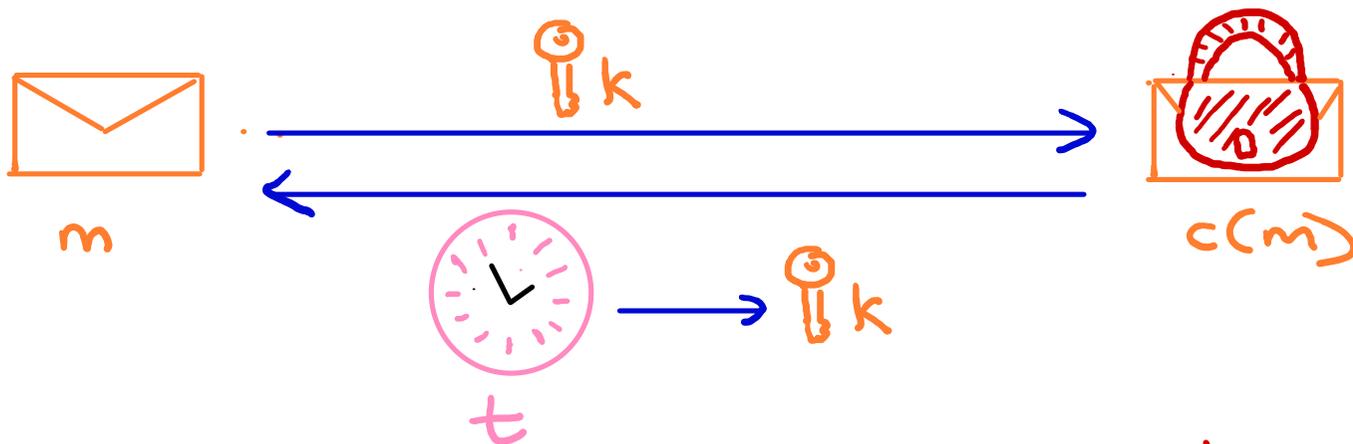
How to implement?

cannot reconstruct k
without knowing $k_i, i=1 \dots 4$

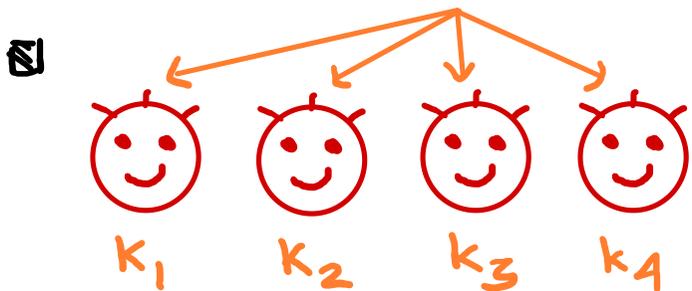


$$k = (k_1, k_2, k_3, k_4)$$

Timed-release crypto



How to implement?



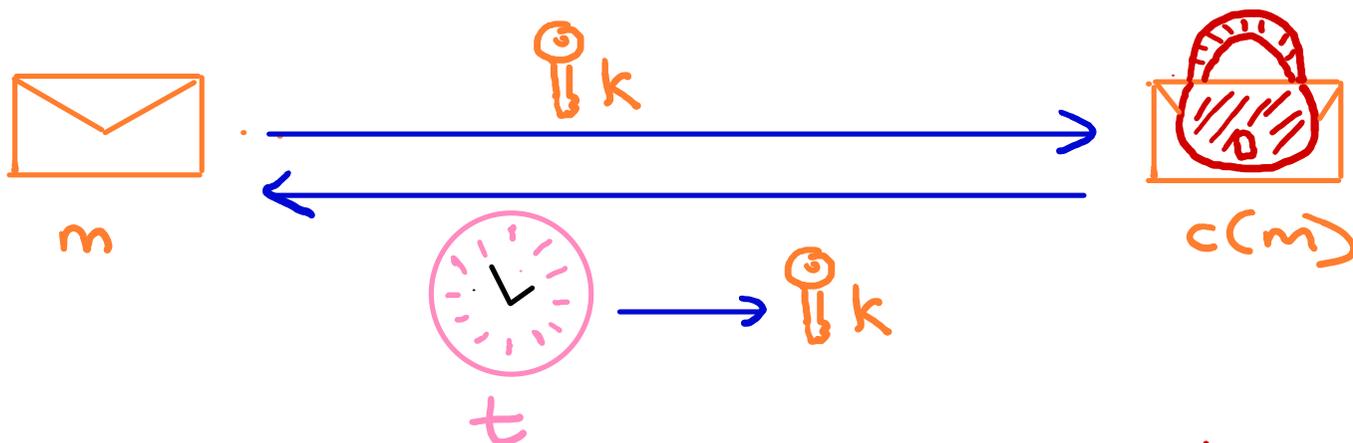
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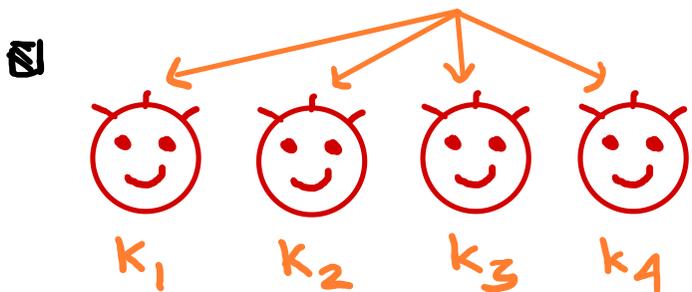
but...

collusion / death / disappearance!

Timed-release crypto



How to implement?



cannot reconstruct k
without knowing $k_i, i=1 \dots 4$

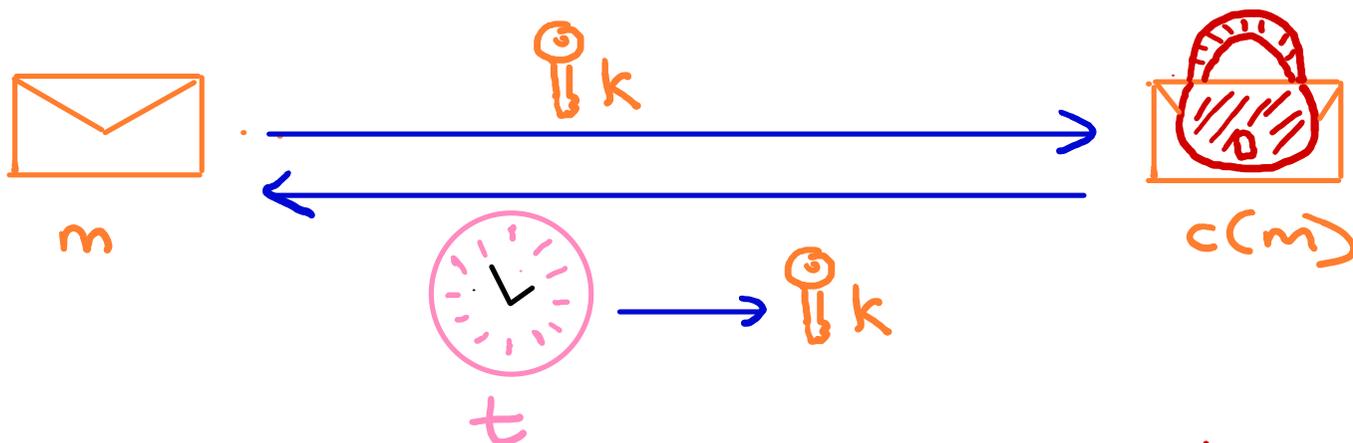
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but...

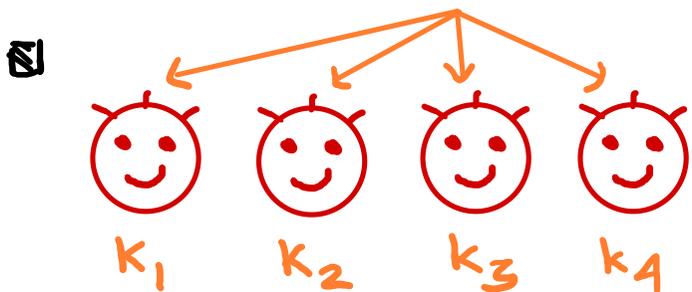
collusion / death / disappearance!
use Shamir's secret sharing!

[S'79]

Timed-release crypto



How to implement?



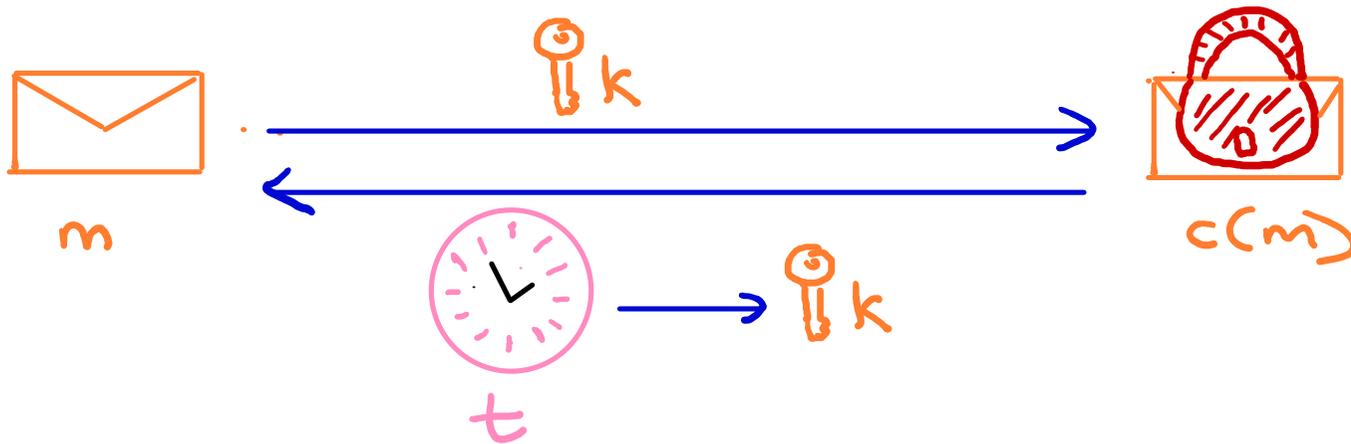
cannot reconstruct k
without knowing $k_i, i=1 \dots 4$

$$k = (k_1, k_2, k_3, k_4)$$

but... still need to rely/trust!
collusion/death/disappearance!
use Shamir's secret sharing!

[S'79]

Timed-release crypto



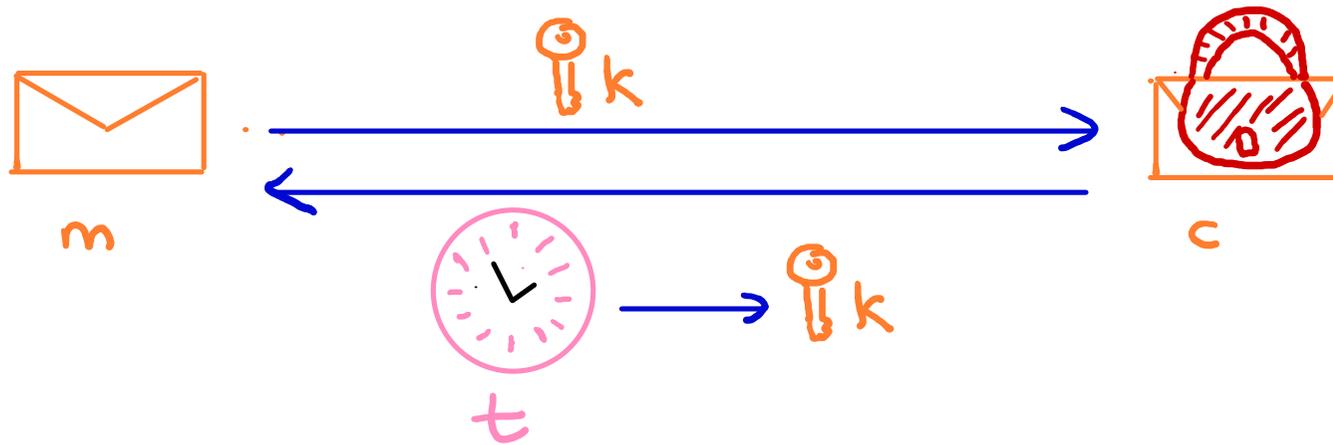
How to implement?

- Use time-lock puzzles!
[RSW'96]

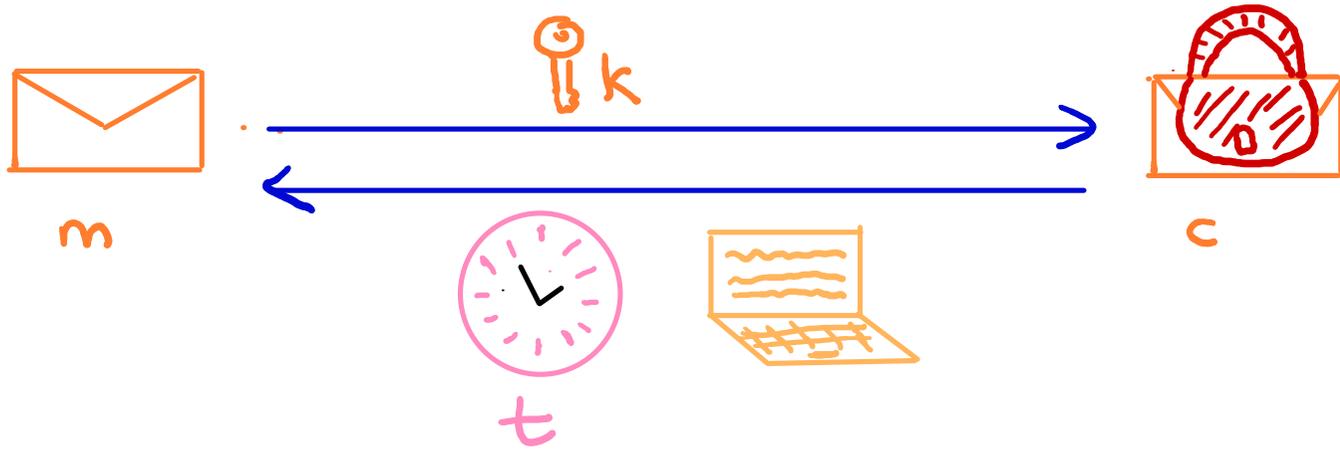
Plan for the afternoon

- Timed-release crypto
- Time-lock puzzles
- Verifiable Delay Functions
- Pietrzak's construction

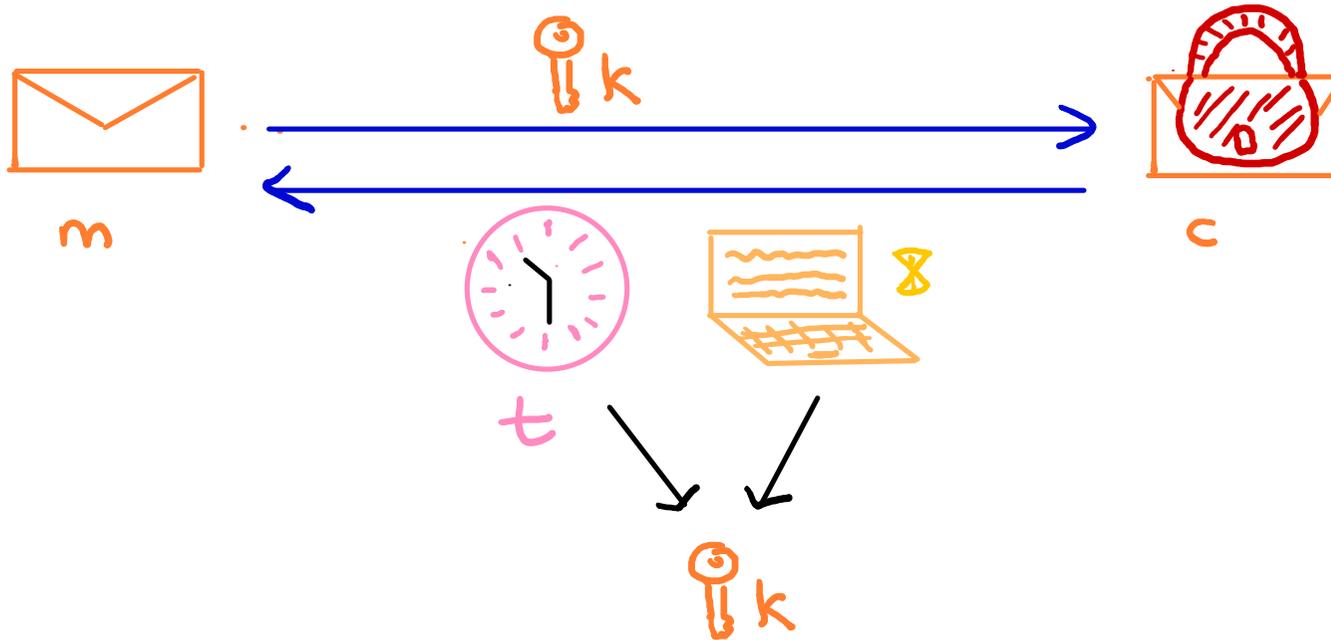
Time-lock Puzzle



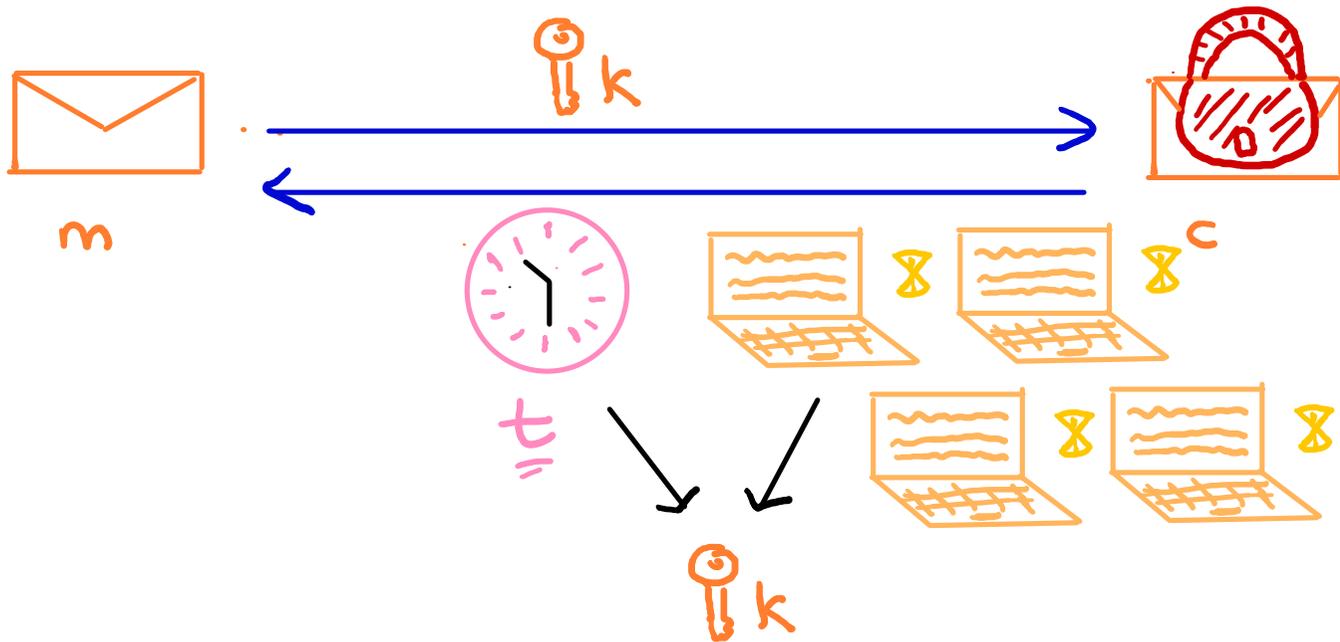
Time-lock Puzzle



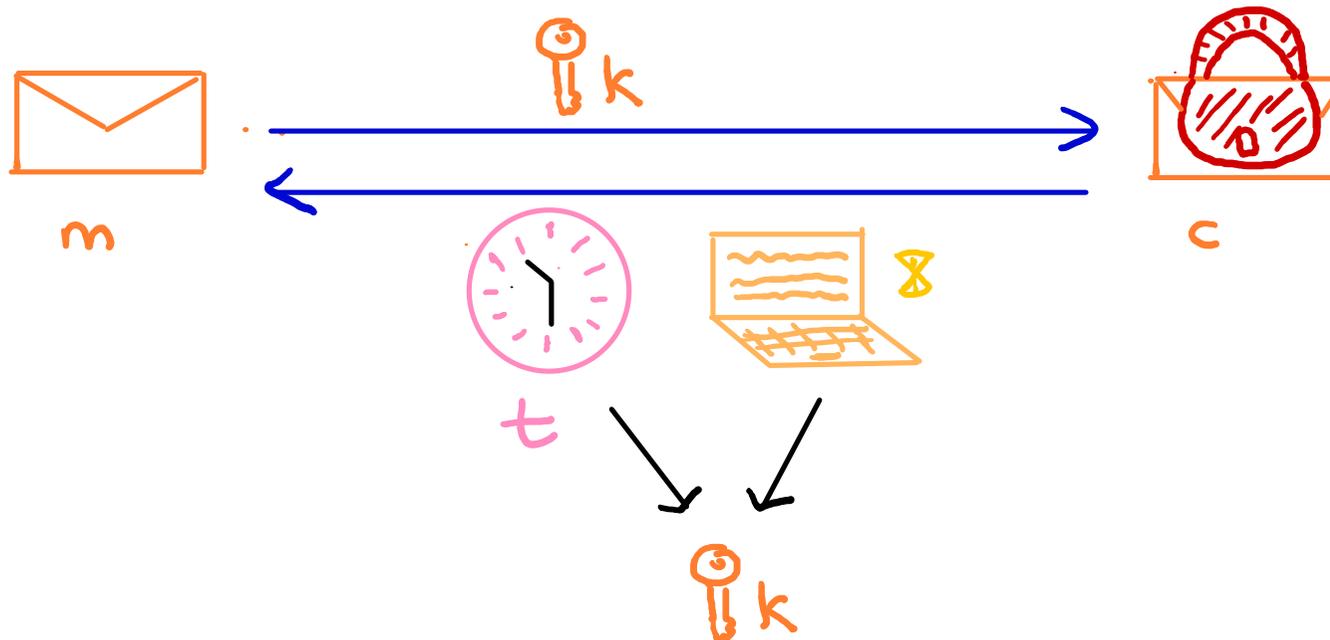
Time-lock Puzzle



Time-lock Puzzle



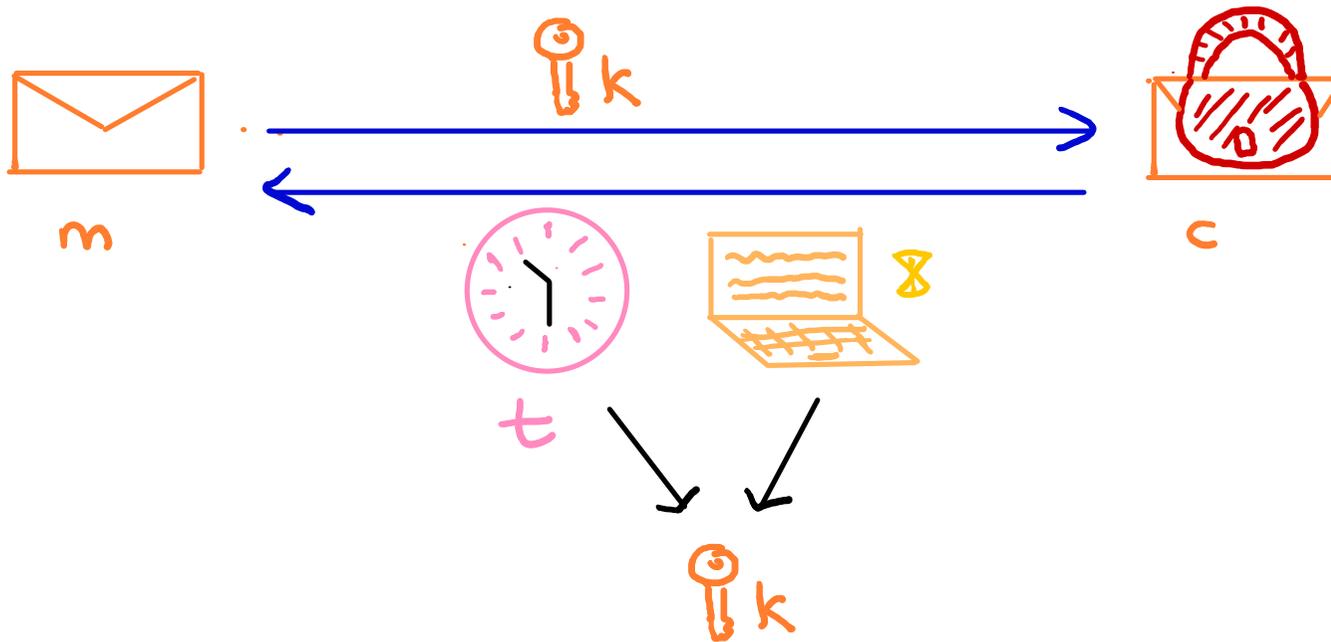
Time-lock Puzzle



Solutions?

- just AES encrypt with $|k| = \log(2st)$ bits,
 $s \rightarrow$ no. of decryptions/s

Time-lock Puzzle

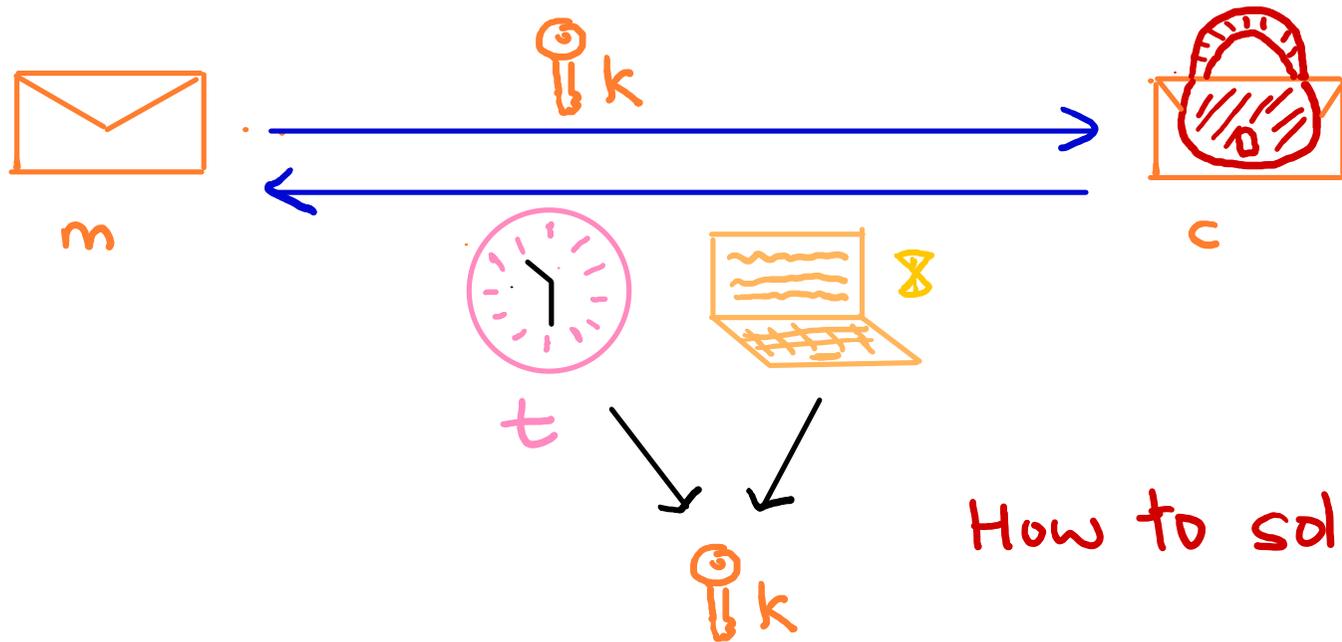


Solutions?

- just AES encrypt with $|k| = \log(2st)$ bits,
 $s \rightarrow$ no. of decryptions/s

and $k \xrightarrow{\text{discard}}$

Time-lock Puzzle



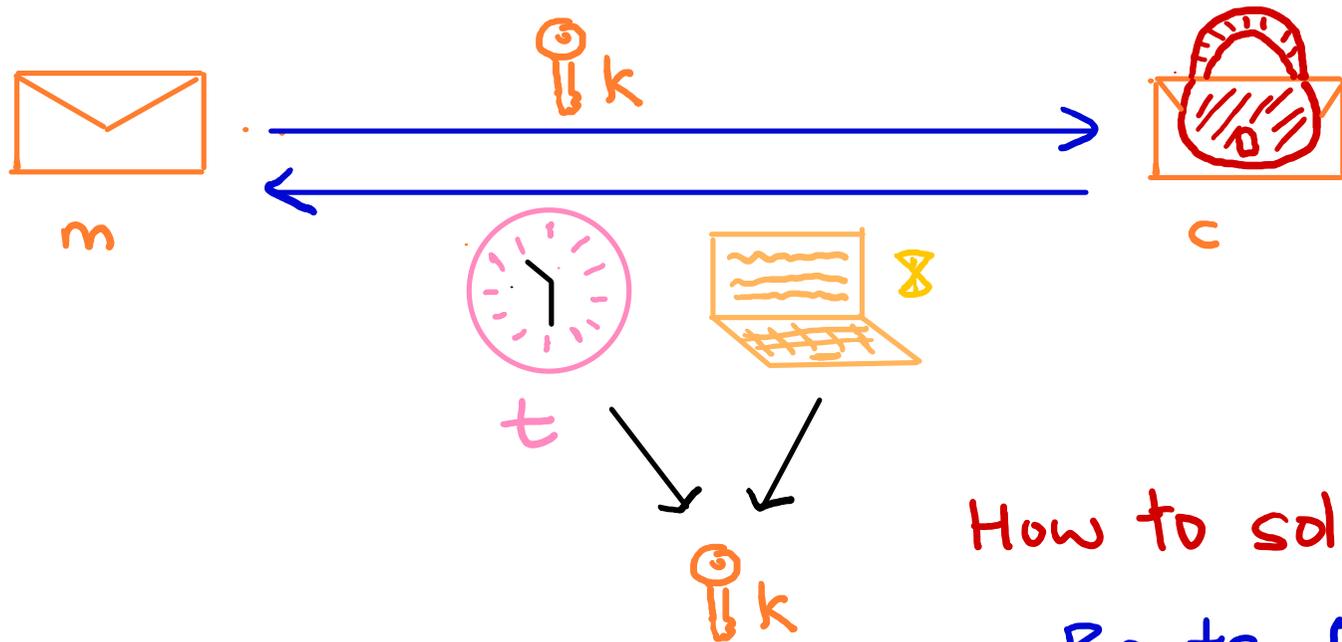
How to solve for k ?

Solutions?

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Time-lock Puzzle



How to solve for k ?

Brute-force!

Solutions?

- just AES encrypt with $|k| = \log(2st)$ bits,
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Time-lock Puzzle

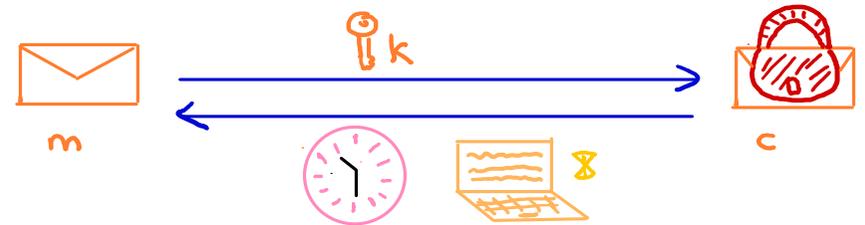
Solutions?

just AES encrypt with $|k| = \log(2st)$ bits,

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Brute-force!



Time-lock Puzzle

Solutions?

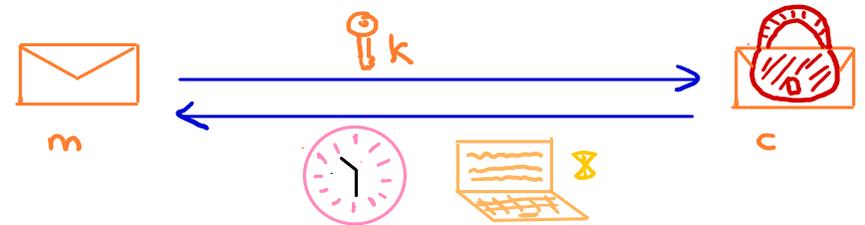
just AES encrypt with $|k| = \log(2st)$ bits,

$s \rightarrow$ no. of decryptions/s

and $k \xrightarrow{\text{discard}}$  How to solve for k ?

problems:

Brute-force!



Time-lock Puzzle

Solutions?

just AES encrypt with $|k| = \log(2st)$ bits, t
 $s \rightarrow$ no. of decryptions/s

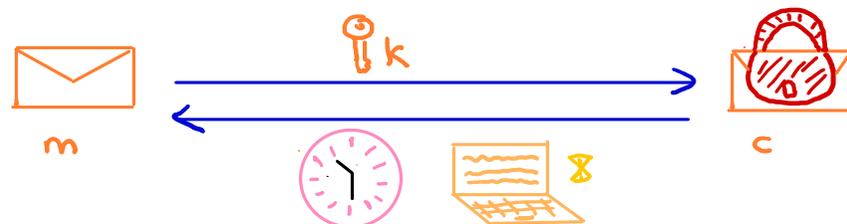
and $k \rightarrow$ discard  How to solve for k ?

Brute-force!

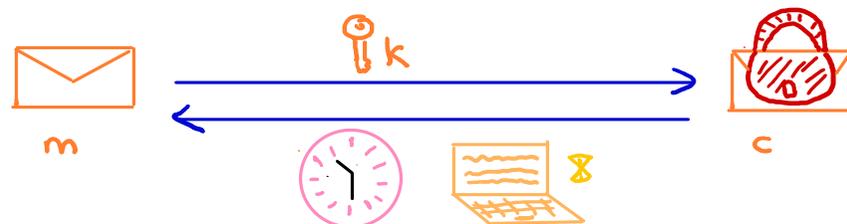
problems:



can find k in less than t



Time-lock Puzzle



Solutions?

just AES encrypt with $|k| = \log(2st)$ bits, t
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problems:



can find k in less than t

needs to be sequential!

Time-lock Puzzle

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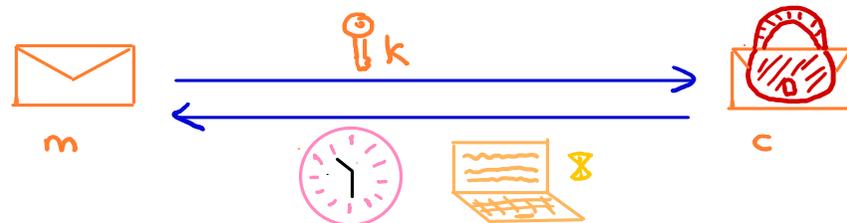
problems:

    can find k in less than t

needs to be sequential!

i.e. k cannot be found in, say,

$$t' = t^c, c < 1$$



Time-lock Puzzle

Solutions?

just AES encrypt with $|k| = \log(2st)$ bits, t
 $s \rightarrow$ no. of decryptions/s

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Brute-force!

problems:

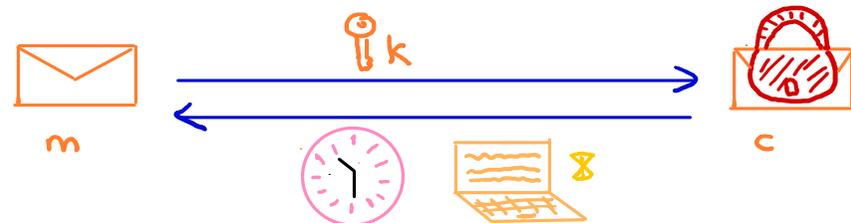


can find k in less than t

needs to be sequential!

i.e. k cannot be found in, say,

$t' = t^c, c < 1$ (allow minor variations)



Time-lock Puzzle

→ RSW construction:

Time-lock Puzzle

→ RSW construction:



m



t



k



c

Time-lock Puzzle

→ RSW construction:



m



t



k

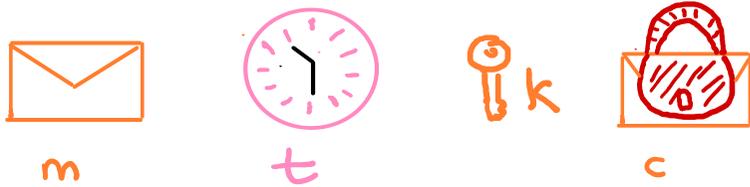


c

Generate $n = pq$

Time-lock Puzzle

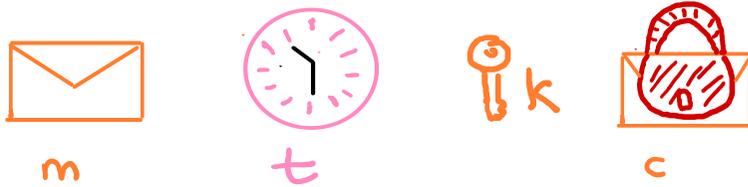
→ RSW construction:



Generate $n = pq$ (for two large random secret primes p, q)

Time-lock Puzzle

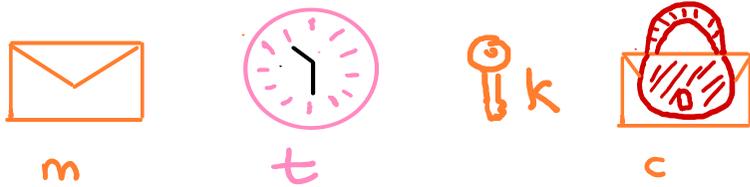
→ RSW construction:



- Generate $n = pq$ (for two large random secret primes p, q)
- Compute $\phi(n) = (p-1)(q-1)$

Time-lock Puzzle

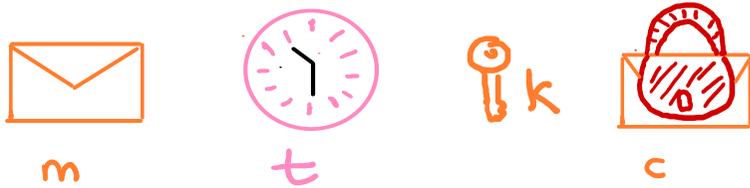
→ RSW construction:



- Generate $n = pq$ (for two large random secret primes p, q)
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- Pick random a ($1 < a < n$)

Time-lock Puzzle

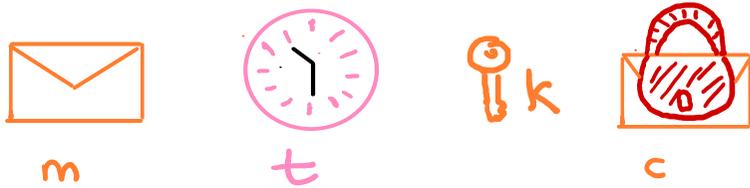
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- Generate $n = pq$ (for two large random secret primes p, q)
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- Pick random a ($1 < a < n$)
- Compute $c_k = k + a^{2^t} \pmod{n}$

Time-lock Puzzle

→ RSW construction:

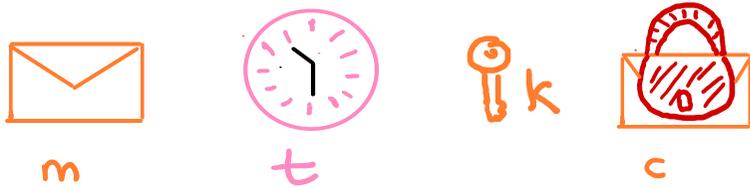


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How?

Time-lock Puzzle

→ RSW construction:

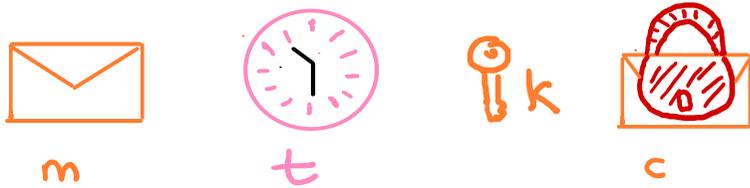


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How? compute $e = 2^T \pmod{\phi(n)}$

Time-lock Puzzle

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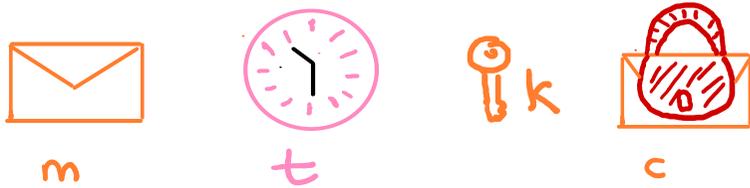


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How? compute $e = 2^T \pmod{\phi(n)}$
 $b = a^e \pmod{n}$

Time-lock Puzzle

→ RSW construction:



- Generate $n = pq$ (for two large random secret primes p, q)
- Compute $\phi(n) = (p-1)(q-1)$
- Pick random a ($1 < a < n$)
- Compute $c_k = k + a^{2^t} \pmod{n}$
- Output (n, a, t, c, c_k) and  anything else

Time-lock Puzzle

→ RSW construction:

Why does this work?

Time-lock Puzzle

→ RSW construction:

Why does this work?

▣ brute-forcing the AES key → infeasible

Time-lock Puzzle

→ RSW construction:

Why does this work?

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fastest way to solve puzzle:

Time-lock Puzzle

→ RSW construction:

Why does this work?

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fastest way to solve puzzle:

$$a \rightarrow a^2 \rightarrow a^{2^2} \rightarrow a^{2^3} \rightarrow \dots \rightarrow a^{2^t} \pmod{n}$$

Time-lock Puzzle

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t sequential steps

Time-lock Puzzle

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but $n \rightarrow \phi(n) \approx$ factoring n !

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Time-lock Puzzle

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fastest way to solve puzzle:

$$a \rightarrow a^2 \rightarrow a^{2^2} \rightarrow a^{2^3} \rightarrow \dots \rightarrow a^{2^t} \pmod{n}$$

t sequential steps

(no known algo. to parallelize)

▣ can easily compute if $\phi(n)$ known

but $n \rightarrow \phi(n) \approx$ factoring n !

infeasible for large p, q

Time-lock Puzzle

→ RSW construction:

What about verifiability of $b = a^{z^t}$?

Time-lock Puzzle

→ RSW construction:

What about verifiability of $b = a^{z^t}$?

simply check if $\text{AES.Dec}(c, c_k - b \pmod{n}) = m$?

Time-lock Puzzle

→ RSW construction:

What about verifiability of $b = a^{z^t}$?

simply check if $AES.Dec(c, c_k - b \pmod{n}) = m$?

What about public and efficient verifiability?

Time-lock Puzzle

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↖ breaks sequentiality!

can trivially compute b if $\phi(n)$ known!

Time-lock Puzzle

→ RSW construction:

What about verifiability of $b = a^{z^t}$?

simply check if $AES.Dec(c, c_k - b \pmod{n}) = m$?

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given (a, b, t) , $b = a^{z^t}$

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→ RSW construction:

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simply check if $AES.Dec(c, c_k - b \pmod{n}) = m$?

What about public and efficient verifiability?
given (a, b, t) , $b = a^{z^t}$, some proof Π so that

Time-lock Puzzle

→ RSW construction:

What about verifiability of $b = a^{2^t}$?

simply check if $AES.Dec(c, c_k - b \pmod{n}) = m$?

What about public and efficient verifiability?

given (a, b, t) , $b = a^{2^t}$, some proof π so that

hmm...



Any Body

(a, b, t, π)

Time-lock Puzzle

→ RSW construction:

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(a, b, t, π)

← should take time $t' \lll t$

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Possible?

Time-lock Puzzle

→ RSW construction:

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simply check if $\text{AES.Dec}(c, c_k - b \pmod{n}) = m$?

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given (a, b, t) , $b = a^{z^t}$, some proof π so that



(a, b, t, π) ← should take time $t' \lll t$

Possible? Yes, using VDFs!

Plan for the afternoon

- Timed-release crypto
- Time-lock puzzles
- Verifiable Delay Functions
- Pietrzak's construction

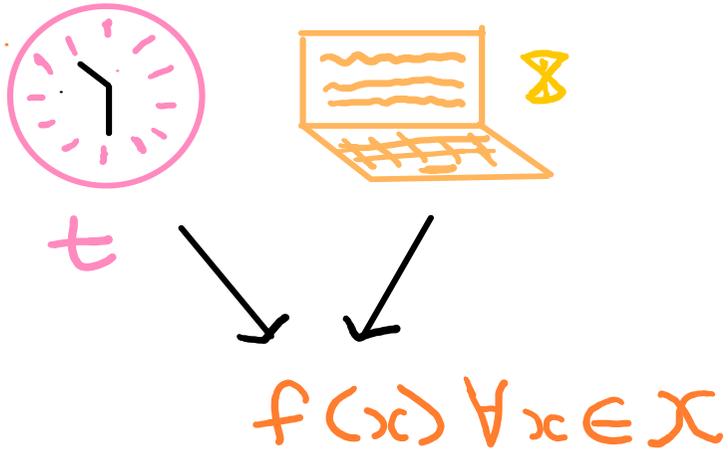
Verifiable Delay Functions [BBBF'18]

Verifiable Delay Functions

→ $f: X \rightarrow Y$

Verifiable Delay Functions

→ $f: X \rightarrow Y$



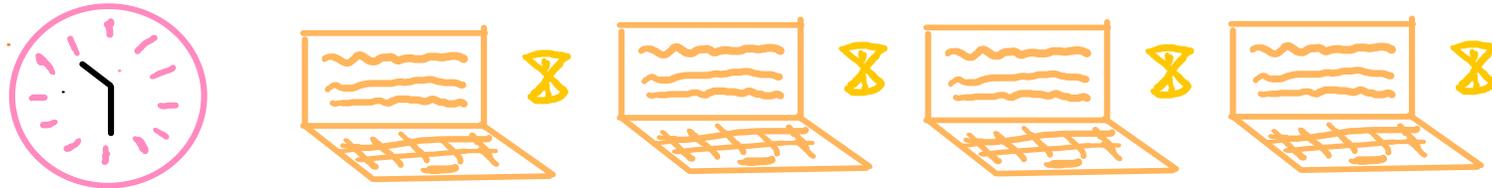
Verifiable Delay Functions

→ $f: X \rightarrow Y$



Verifiable Delay Functions

→ $f: X \rightarrow Y$



t

$f(x) \forall x \in X$



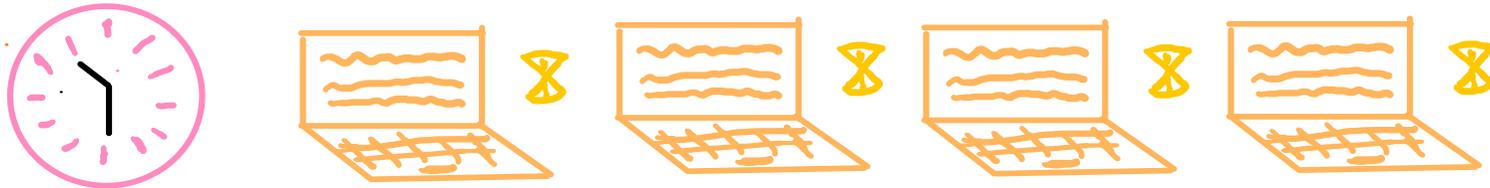
Any Body

$(x, y, t, \pi), y = f(x)$

(correctness)

Verifiable Delay Functions

$$\rightarrow f: X \rightarrow Y$$



t

$$f(x) \forall x \in X$$



Any Body

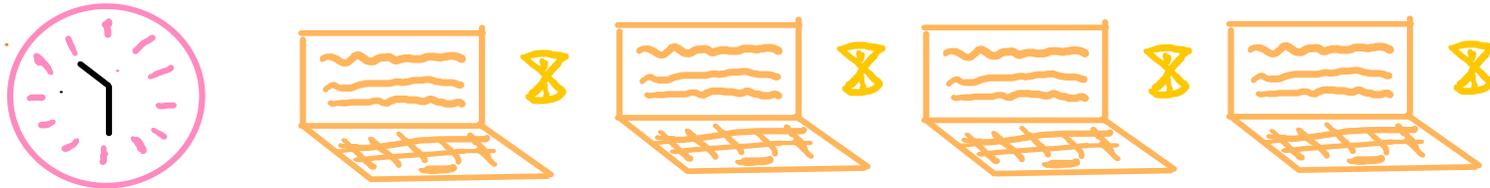
$$(x, y, t, \pi), y = f(x)$$

$$t' \lll t$$

↑ efficiency

Verifiable Delay Functions

→ $f: X \rightarrow Y$



t

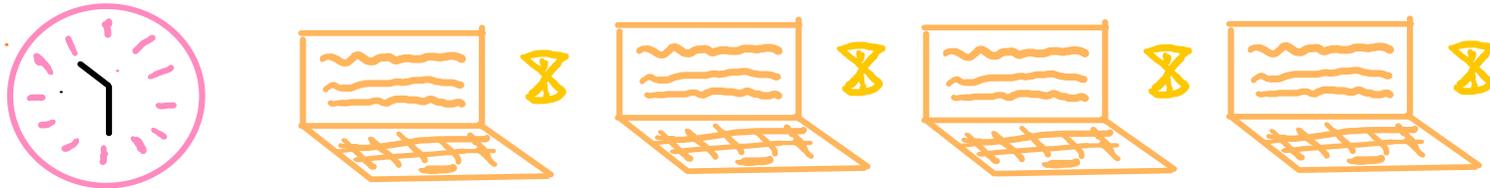
$f(x) \forall x \in X$



$(x, y, t, \pi), y \neq f(x)$
(soundness)

Verifiable Delay Functions

→ $f: X \rightarrow Y$



t

$f(x) \forall x \in X$



$(x, y, t, \pi), y \neq f(x)$

(soundness)

unique valid output

for all $x \in X$

Verifiable Delay Functions

→ More specifically, a **tuple** of 3 algorithms:

Verifiable Delay Functions

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▣ $\text{Setup}(\lambda, T) \rightarrow \text{PP}$

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 ↑
 security

Verifiable Delay Functions

→ More specifically, a tuple of 3 algorithms:

▣ $\text{Setup}(\lambda, T) \rightarrow \text{PP}$ ← public param
 ↑ ↑
 security delay

Verifiable Delay Functions

→ More specifically, a tuple of 3 algorithms:

▣ $\text{Setup}(\lambda, T) \xrightarrow{\$} \text{PP}$ ← public param (set x, y)
↑ security ↑ delay

▣ $\text{Eval}(\text{PP}, x) \rightarrow (y, \pi)$
↑ input ↓ proof
 ↑ output

▣ $\text{Verify}(\text{PP}, x, y, \pi) \rightarrow \{0, 1\}$
 ↑ output if $(y, \pi) \leftarrow \text{Eval}(\text{PP}, x)$

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Sometimes, also

▣ $\text{TEval}(t, \text{PP}, x) \rightarrow (y, \pi)$

$\text{PP} \leftarrow \text{Setup}(\lambda, t)$
 $x \in X$

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 ↑ trapdoor

$\text{PP} \leftarrow \text{Setup}(\lambda, t)$
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▣ $\text{TEval}(td, \text{PP}, x) \rightarrow (y, \pi)$
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Sometimes, also

▣ $\text{TEval}(t_d, \text{PP}, x) \rightarrow (y, \pi)$

Keep secret! trapdoor → use to compute (y, π) in $T' \lll T$

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Verifiable Delay Functions

→ Now, a concrete construction?

Verifiable Delay Functions

→ Now, a concrete construction?

RSW goes publicly verifiable!

Plan for the afternoon

- Timed-release crypto
- Time-lock puzzles
- Verifiable Delay Functions
- Pietrzak's construction

Pietrzak's VDF [Pie'19]

Pietrzak's VDF

→ Let's keep things simple!

Pietrzak's VDF

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Without **loss of generality**

set $T = 2^k, k \in \mathbb{N}$

Pietrzak's VDF

→ Let's keep things simple!

Without **loss of generality**

set $T = 2^k, k \in \mathbb{N}$

$$f: \mathbb{G} \rightarrow \mathbb{G}, h = f(g) = g^{2^T} \text{ (in } \mathbb{G}\text{)}$$

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Setup(λ, T) \longrightarrow pp := (\mathbb{G}, T)

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But how does $\text{Verify}(\text{pp}, g, h, \pi)$ work?

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let's make it **interactive** for now...

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Pat



Victor

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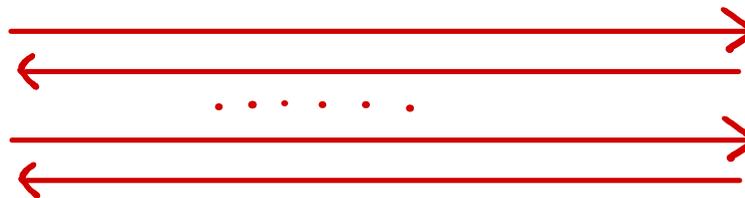
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Pat



Victor

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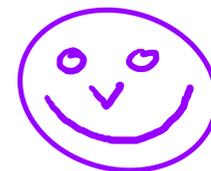
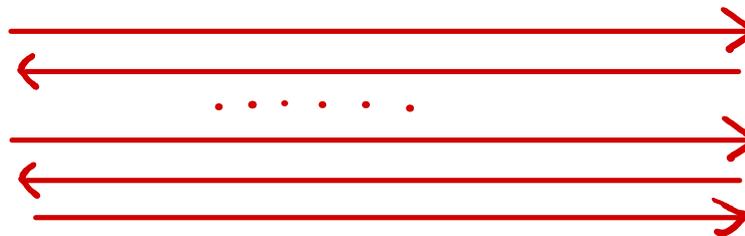
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Pat



Victor

Pietrzak's VDF

Halving subprotocol:
(or meet-in-the-middle)

Pietrzak's VDF

Halving subprotocol:

(or meet-in-the-middle)

$$h = g^{2^T} \quad (\text{in } \mathbb{G})$$

↙ $2^{T/2}$

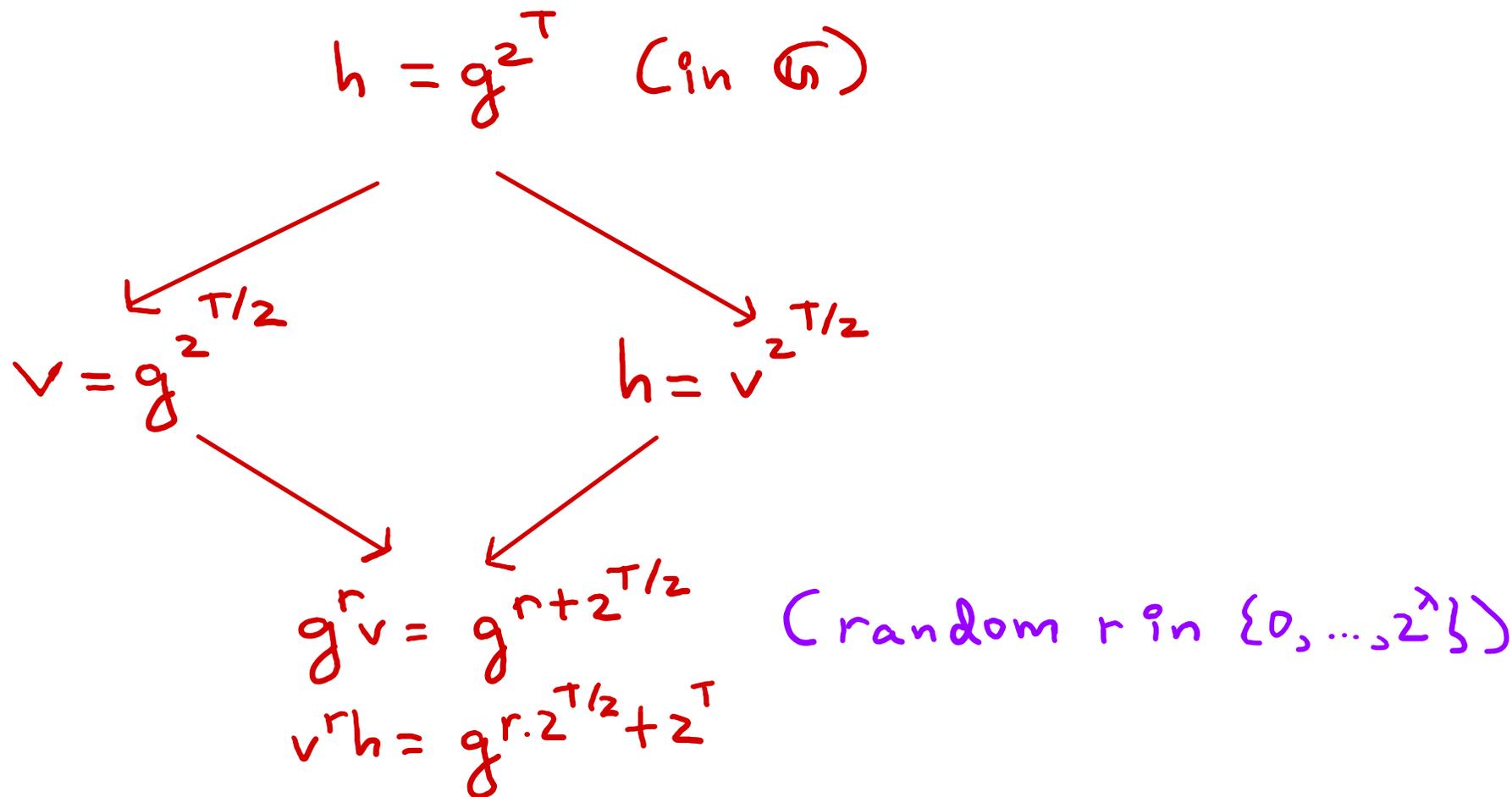
$$v = g^{2^{T/2}}$$

↘ $2^{T/2}$

$$h = v^{2^{T/2}}$$

Pietrzak's VDF

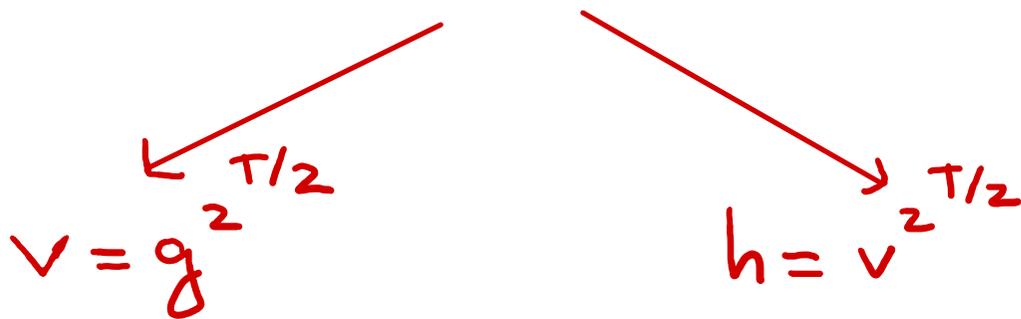
Halving subprotocol:
(or meet-in-the-middle)



Pietrzak's VDF

Halving subprotocol:
(or meet-in-the-middle)

$$h = g^{z^T} \quad (\text{in } \mathbb{G})$$

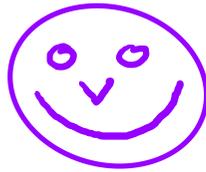
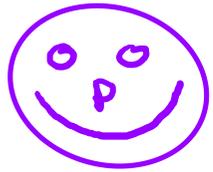


$T/2$ sq. $\left\{ \begin{array}{l} g^r v = g^{r+z^{T/2}} \\ v^r h = g^{r \cdot z^{T/2} + z^T} \end{array} \right.$

(random r in $\{0, \dots, 2^{\lambda}\}$)

Pietrzak's VDF

Halving subprotocol:



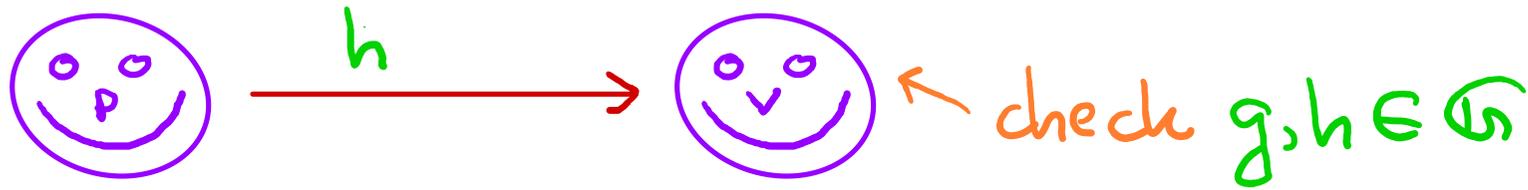
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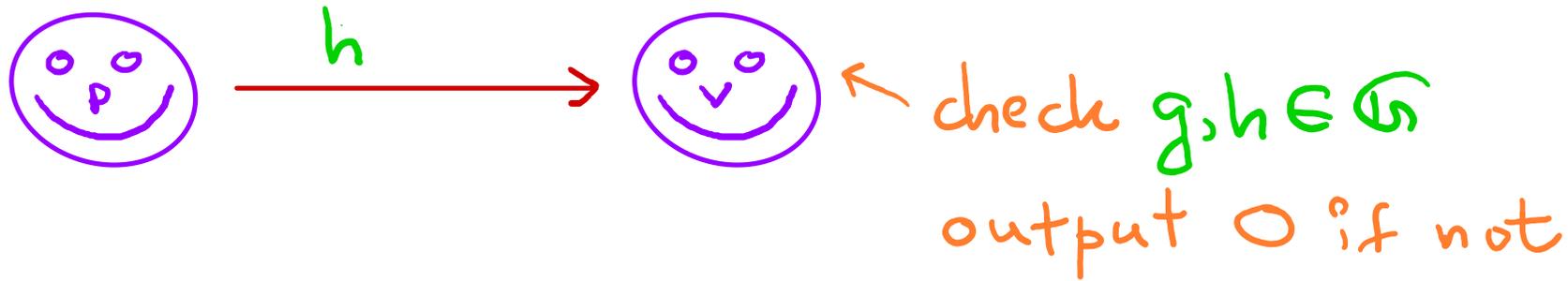
Pietrzak's VDF

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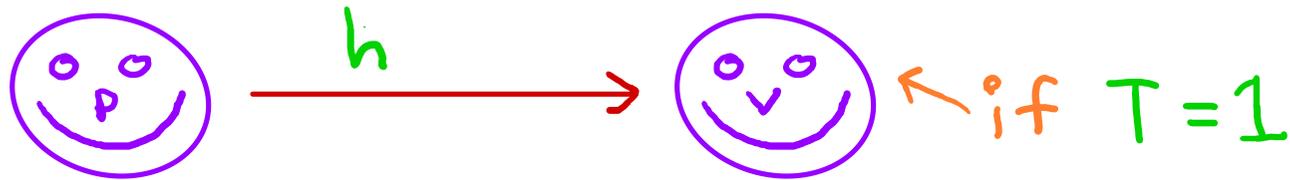
Pietrzak's VDF

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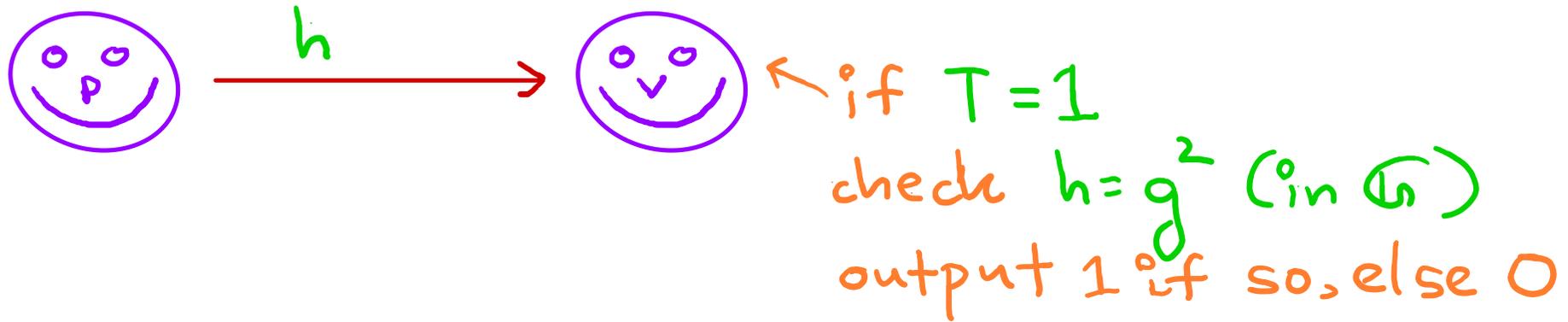
Pietrzak's VDF

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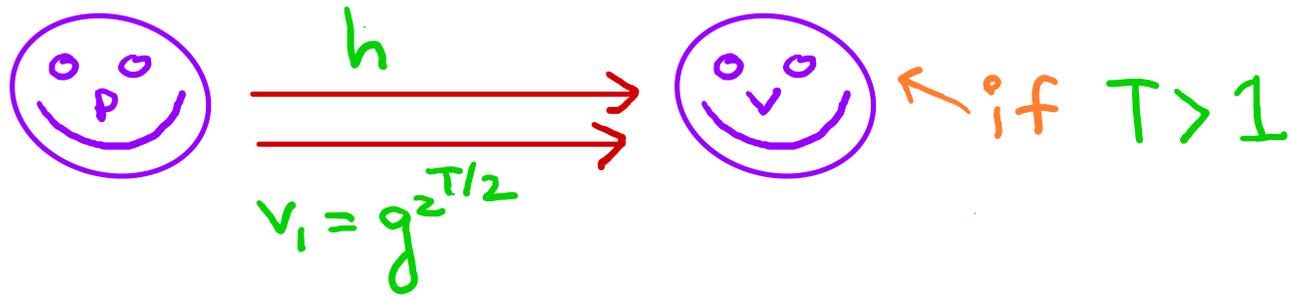
Pietrzak's VDF

Halving subprotocol:



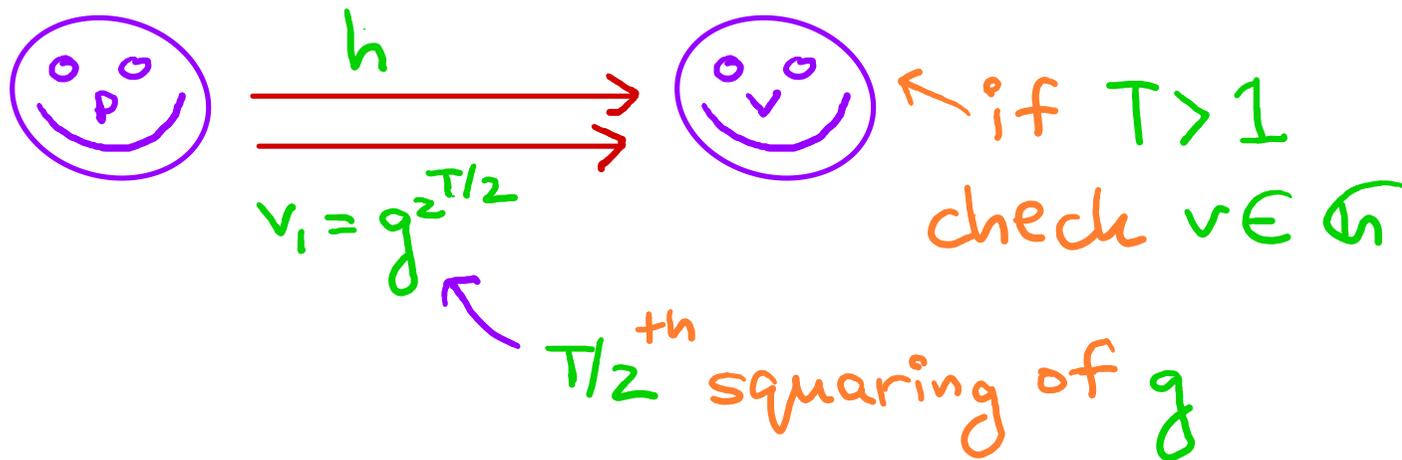
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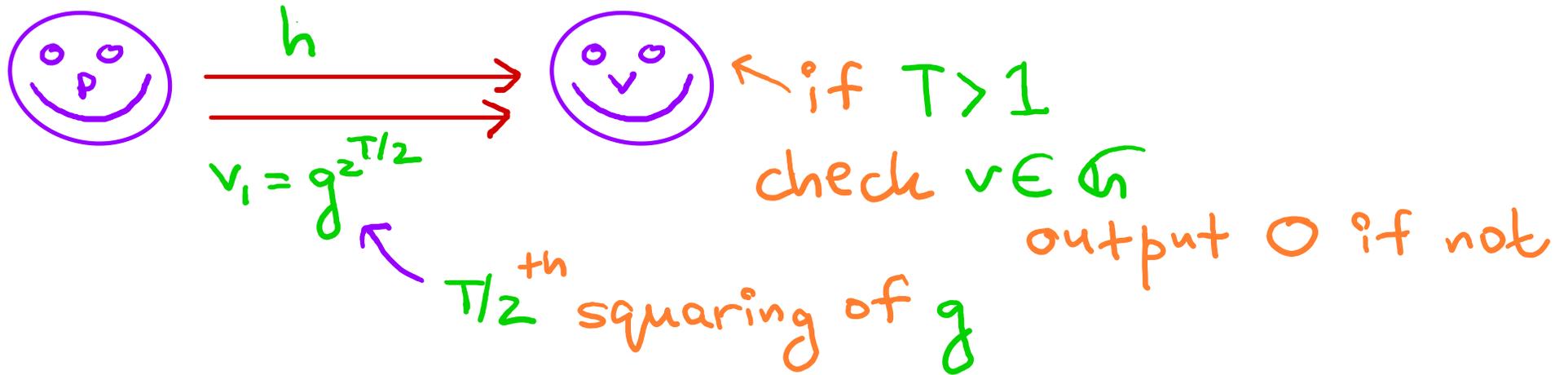
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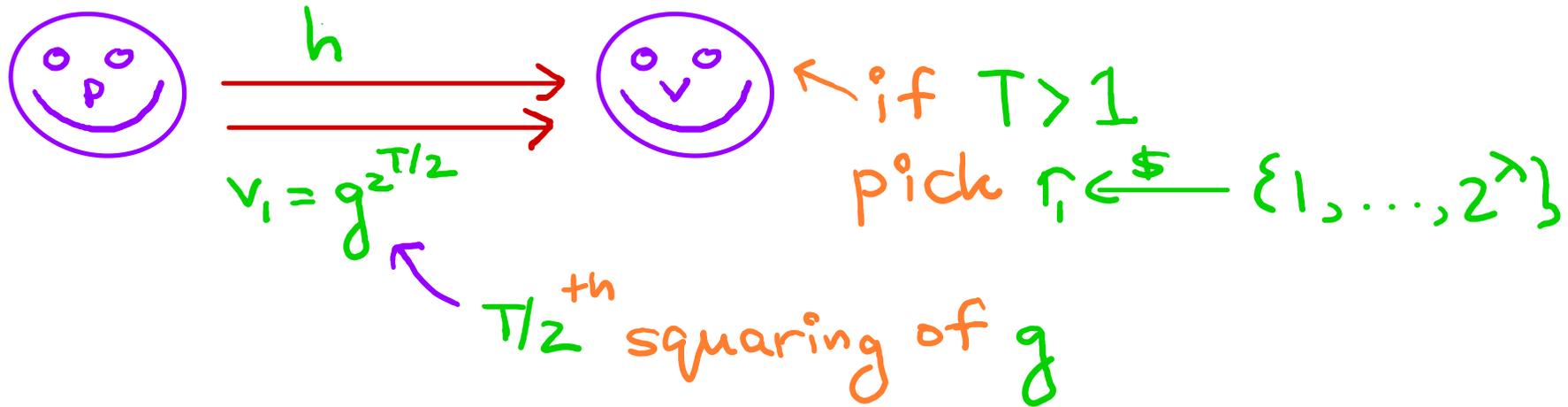
Pietrzak's VDF

Halving subprotocol:



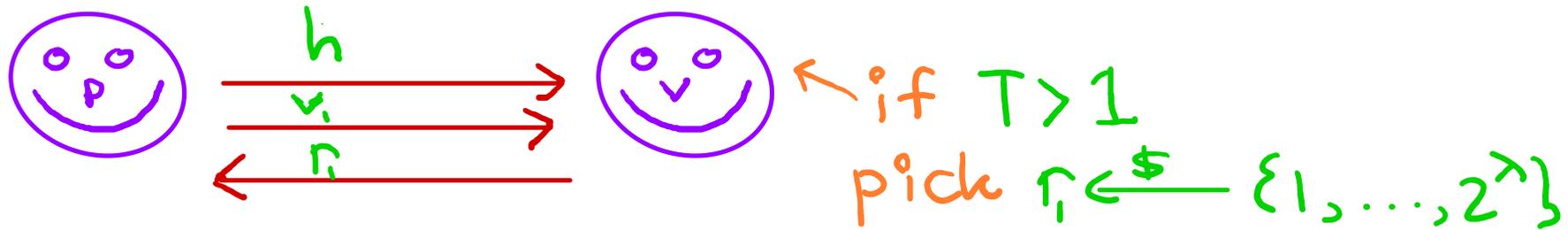
Pietrzak's VDF

Halving subprotocol:



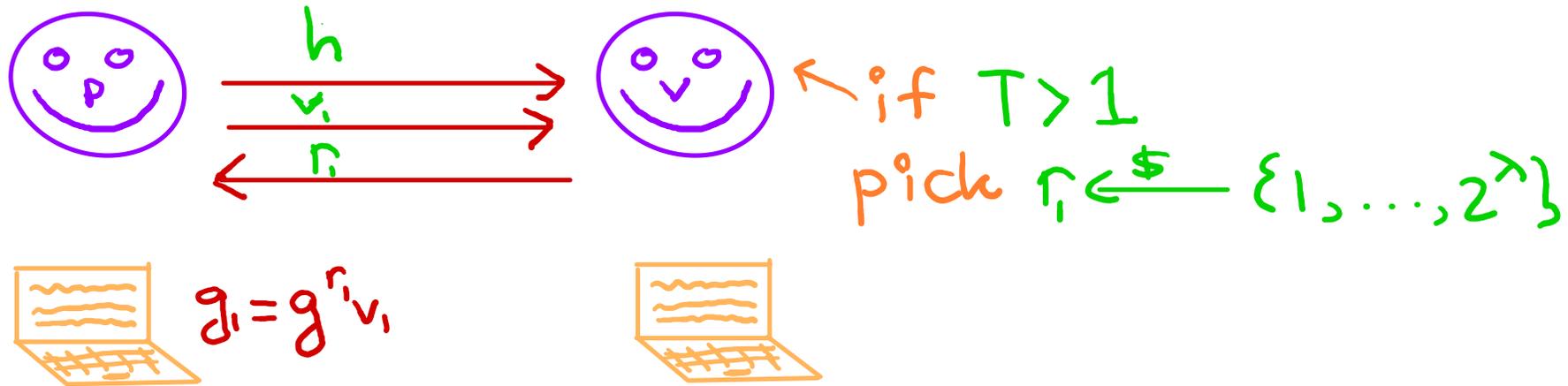
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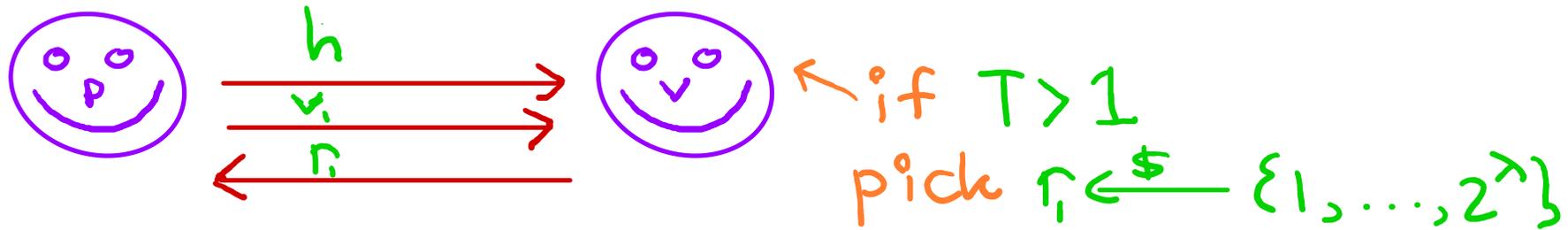
Pietrzak's VDF

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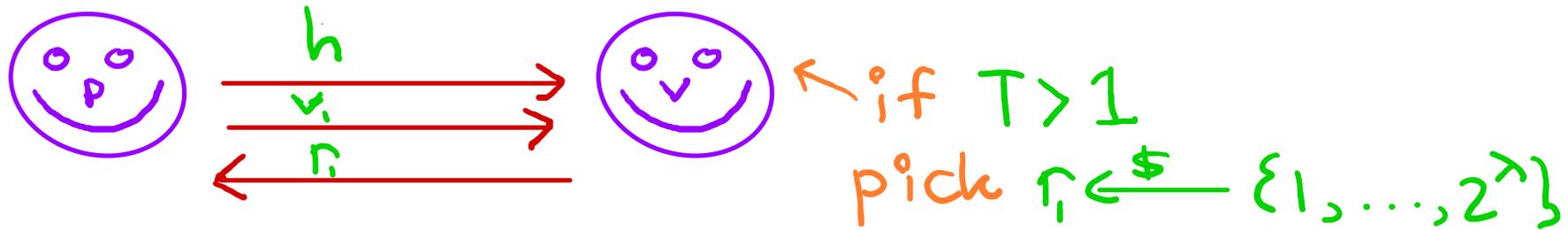


Equation defining the variables g_i and h_i :

$$g_i = g^{r_i} v_i, h_i = v_i^{r_i} h$$

Pietrzak's VDF

Halving subprotocol:



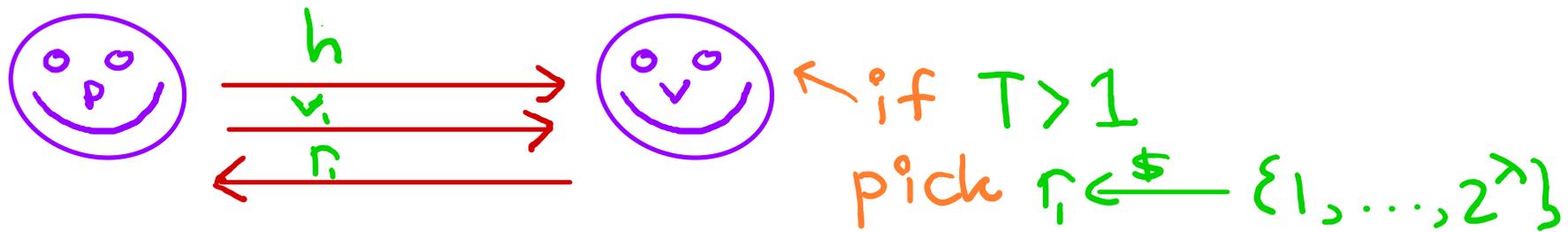
Two laptops are shown, one on the left and one on the right, representing the parties in the protocol. Between them, the following equations are written:

$$g_i = g^{r_i} v_i, h_i = v_i^{r_i} h$$

(in G)

Pietrzak's VDF

Halving subprotocol:



Equation defining the input g_i and h_i for the next recursive layer, where v_i is the value from the previous step:

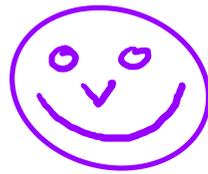
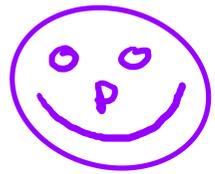
$$g_i = g^{r_i} v_i, h_i = v_i^{r_i} h$$

(in G)

input for next recursive layer

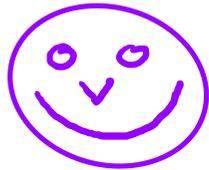
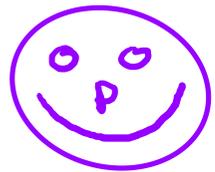
Pietrzak's VDF

Halving subprotocol:



Pietrzak's VDF

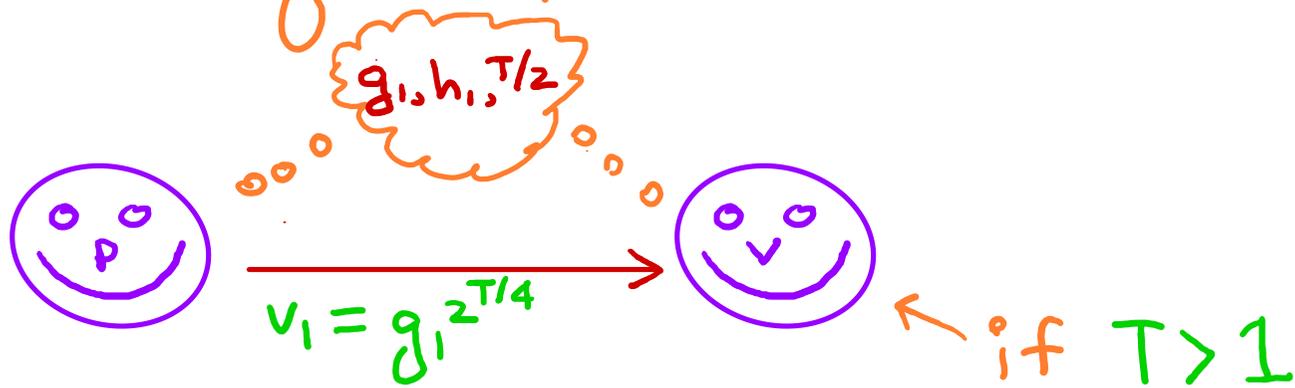
Halving subprotocol:



← if $T=1$
check $h = g^2$ (in \mathbb{G})
output 1 if so, else 0
STOP

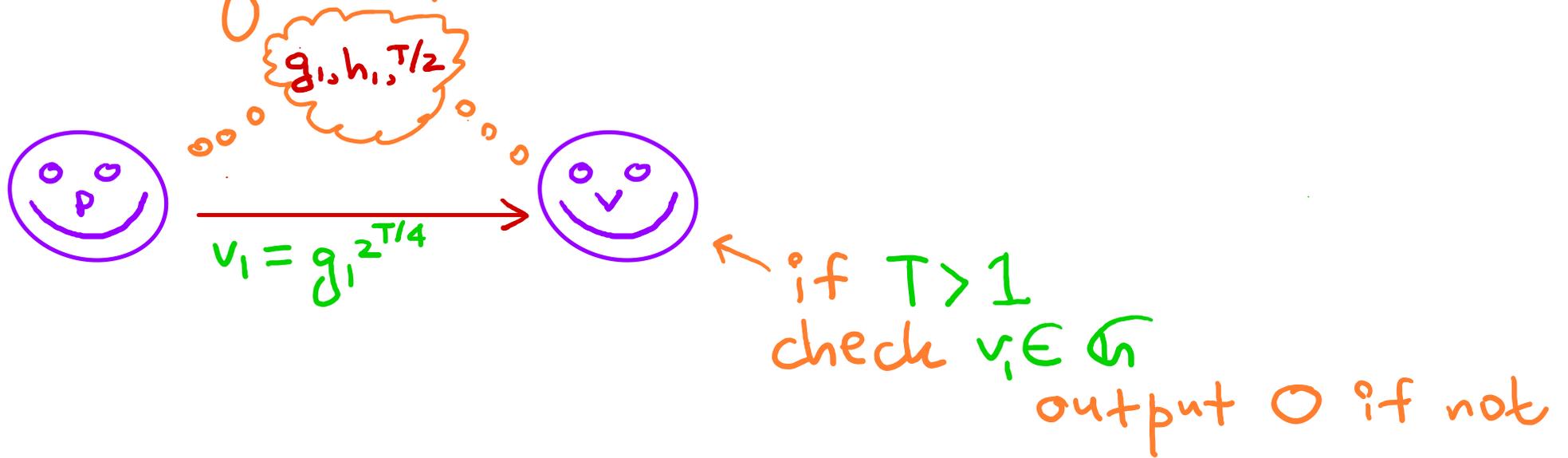
Pietrzak's VDF

Halving subprotocol:



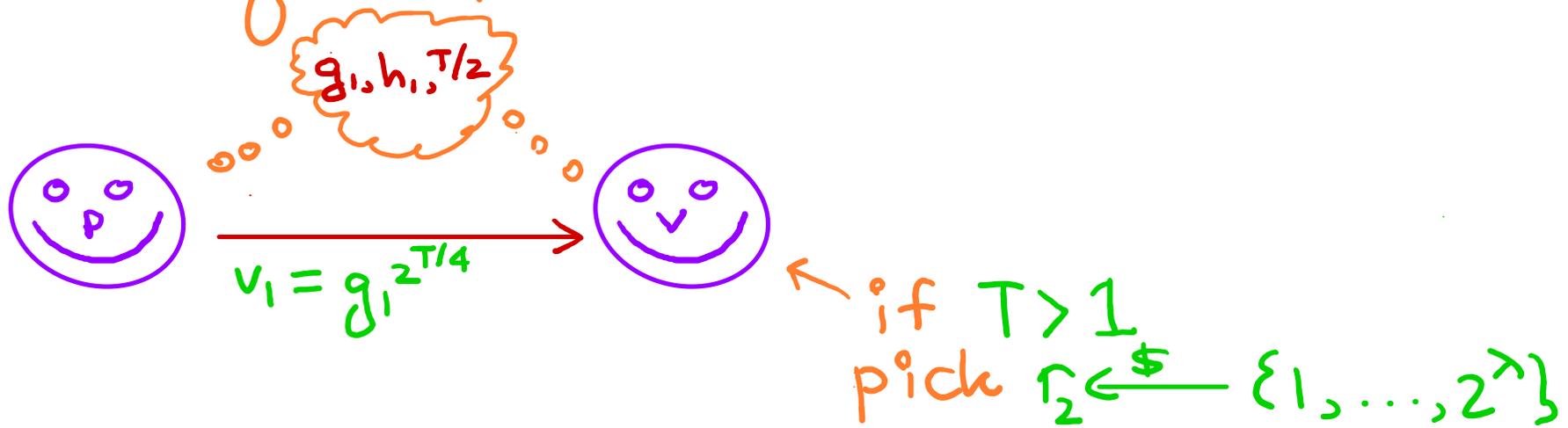
Pietrzak's VDF

Halving subprotocol:



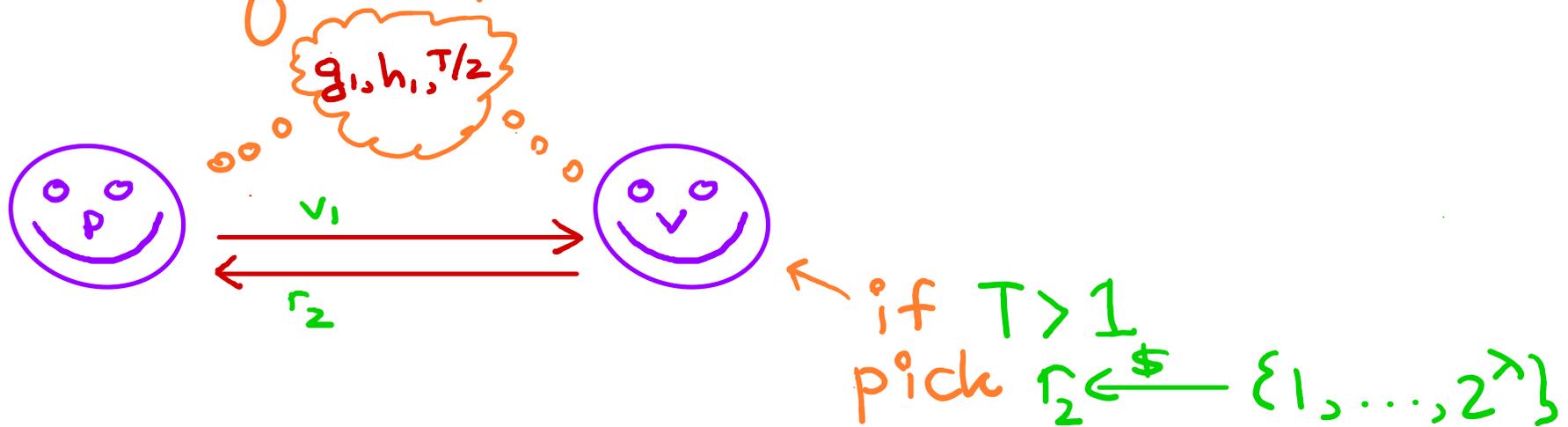
Pietrzak's VDF

Halving subprotocol:



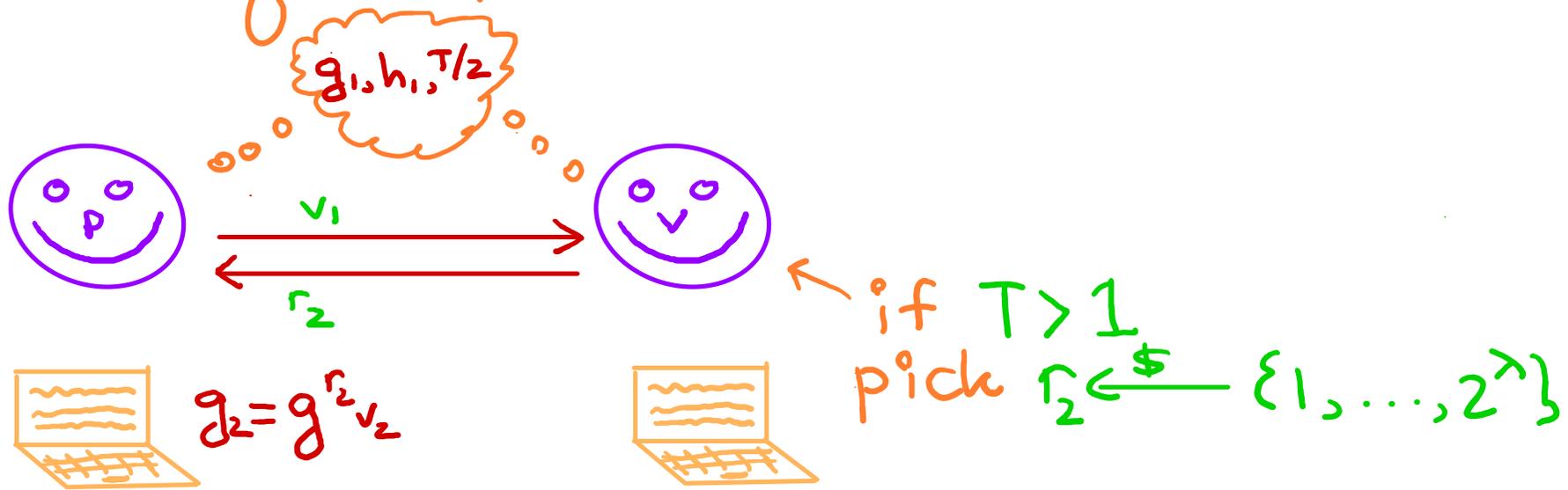
Pietrzak's VDF

Halving subprotocol:



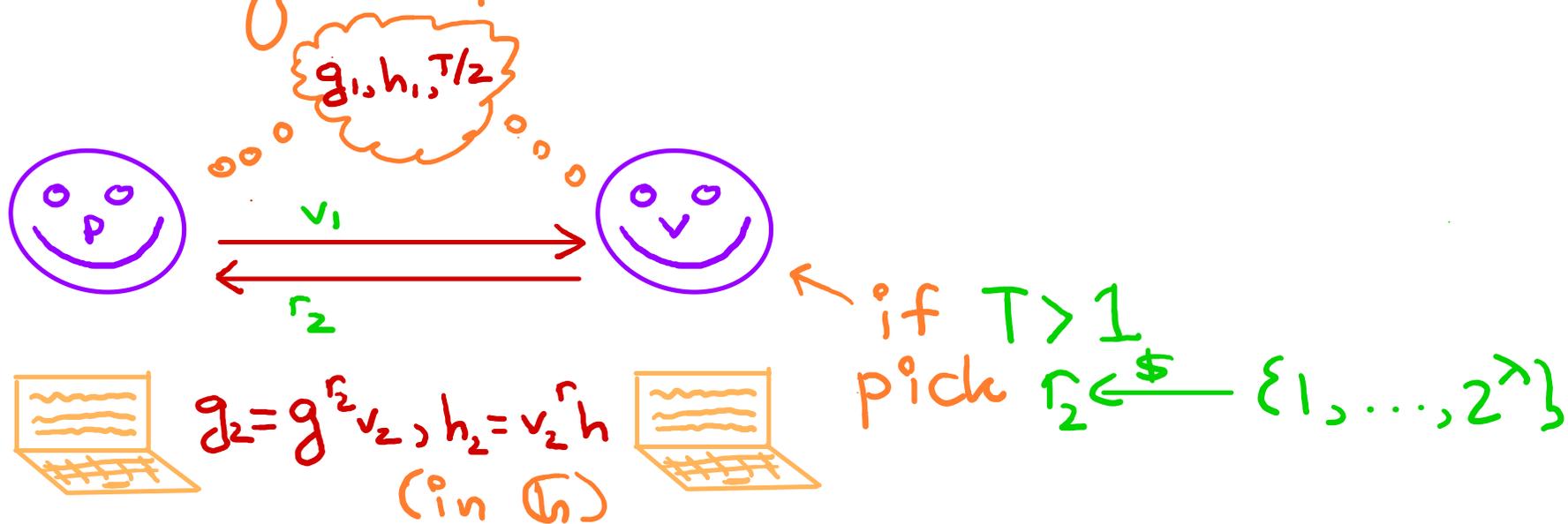
Pietrzak's VDF

Halving subprotocol:



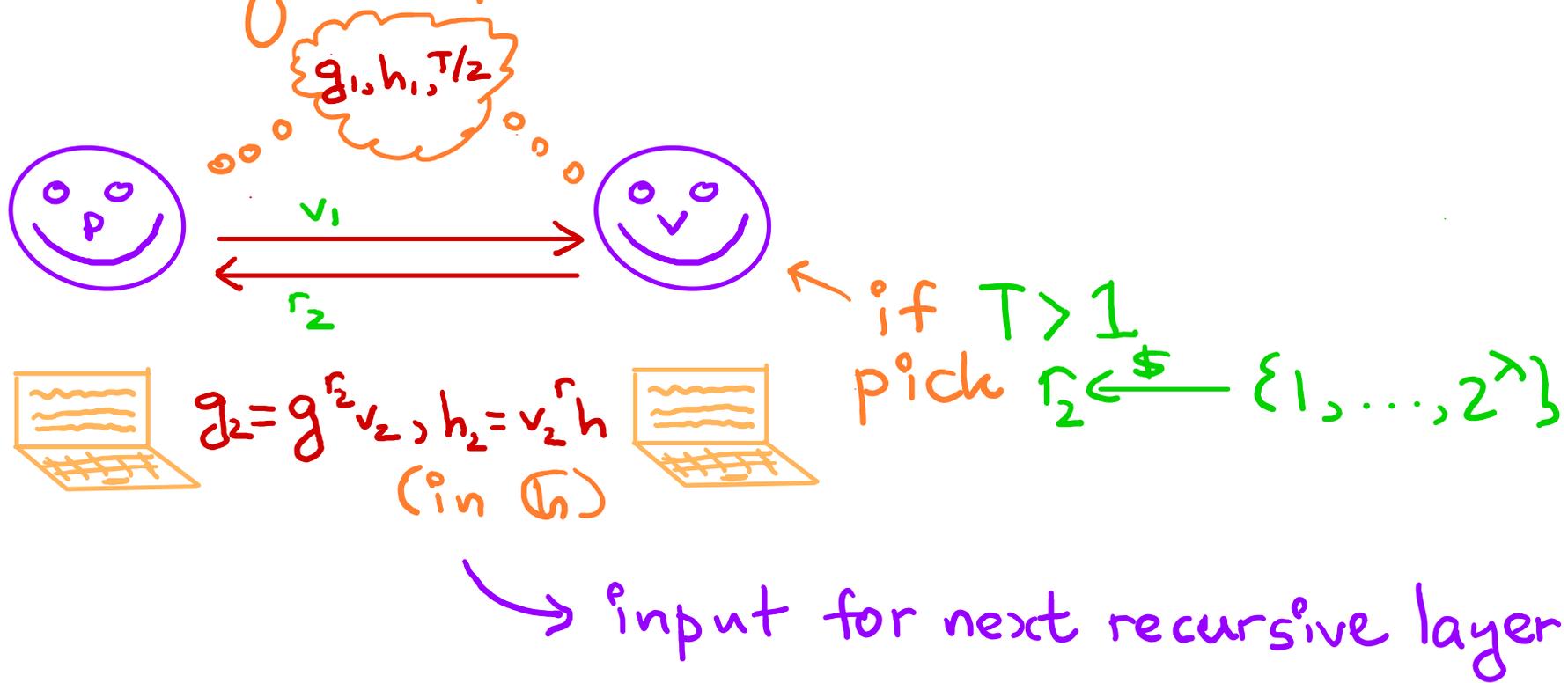
Pietrzak's VDF

Halving subprotocol:



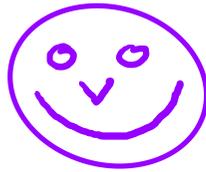
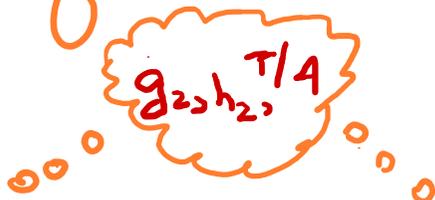
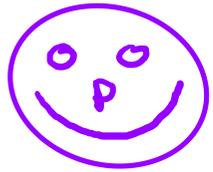
Pietrzak's VDF

Halving subprotocol:



Pietrzak's VDF

Halving subprotocol:



Pietrzak's VDF

Halving subprotocol:



rinse and repeat!

Pietrzak's VDF

Halving subprotocol:



rinse and repeat!

$\log_2 T$ rounds

Pietrzak's VDF

Halving subprotocol:



rinse and repeat!

$\log_2 T$ rounds \rightarrow can be made **non-interactive**

Pietrzak's VDF

Halving subprotocol:



rinse and repeat!

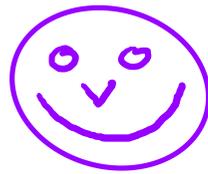
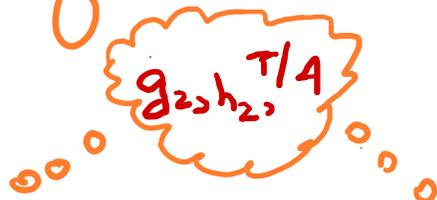
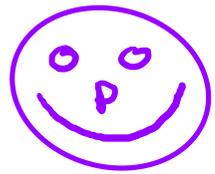
$\log_2 T$ rounds \rightarrow can be made **non-interactive**

(Fiat-Shamir in ROM)

[FS'86]

Pietrzak's VDF

Halving subprotocol:



rinse and repeat!

$\log_2 T$ rounds \rightarrow can be made **non-interactive**

(Fiat-Shamir in ROM)

[FS'86]

then Π has $\log_2 T$ elements

Pietrzak's VDF

Halving subprotocol:



rinse and repeat!

$\log_2 T$ rounds \rightarrow can be made **non-interactive**

(Fiat-Shamir in ROM)

[FS'86]

then Π has $\log_2 T$ elements

let's unwind the recursion for $T=8$!

Pietrzak's VDF

Halving subprotocol for $T=8$:

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} \circ$$

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$

$$v_1 = g^{2^{8/2}}$$

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$

$$v_1 = g^{2^{8/2}} = g^{16}$$

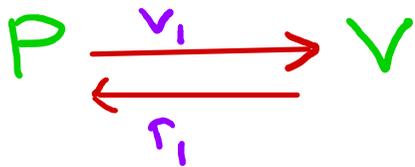
$$P \xrightarrow{v_1} V$$

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$

$$v_1 = g^{2^{8/2}} = g^{16}$$



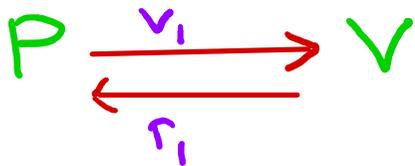
Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$

$$v_1 = g^{2^{8/2}} = g^{16}$$

$$g_1 = g^{r_1} v_1$$

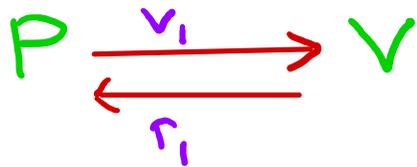


Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$
$$v_1 = g^{2^{8/2}} = g^{16}$$

$$g_1 = g^{\tilde{r}} v_1 = g^{\tilde{r}} \cdot g^{16} = g^{\tilde{r} + 16}$$



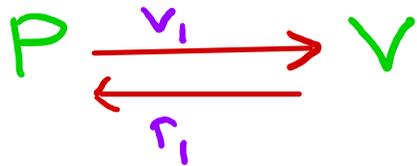
Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$
$$v_1 = g^{2^{8/2}} = g^{16}$$

$$g_1 = g^{r_1} v_1 = g^{r_1} \cdot g^{16} = g^{r_1 + 16}$$

$$h_1 = v_1^{r_1} h$$

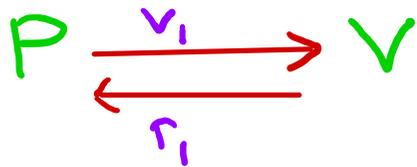


Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$

$$v_1 = g^{2^{8/2}} = g^{16}$$



$$g_1 = g^{r_1} v_1 = g^{r_1} \cdot g^{16} = g^{r_1 + 16}$$

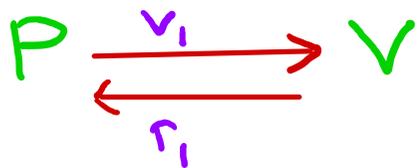
$$h_1 = v_1^{r_1} h = g^{16 r_1} \cdot g^{256} = g^{16 r_1 + 256}$$

Pietrzak's VDF

Halving subprotocol for $T=8$:

$$h = g^{2^8} = g^{256}$$

$$v_1 = g^{2^{8/2}} = g^{16}$$



$$g_1 = g^{r_1} v_1 = g^{r_1} \cdot g^{16} = g^{r_1 + 16}$$

$$h_1 = v_1^{r_1} h = g^{16 r_1} \cdot g^{256} = g^{16 r_1 + 256}$$

input for next recursive layer:

$$T=4, g_1, h_1$$

Pietrzak's VDF

Halving subprotocol for $T=4$:

$$g_1 = g^{r_1 + 16}$$

$$h_1 = g^{16r_1 + 256}$$

$$v_2 = g_1^{2^{4/2}}$$

Pietrzak's VDF

Halving subprotocol for $T=4$:

$$g_1 = g^{r_1 + 16}$$

$$h_1 = g^{16r_1 + 256}$$

$$v_2 = g_1^{2^{4/2}} = (g^{r_1 + 16})^4 = g^{4r_1 + 64}$$

Pietrzak's VDF

Halving subprotocol for $T=4$:

$$g_1 = g^{r_1 + 16}$$

$$h_1 = g^{16r_1 + 256}$$

$$v_2 = g_1^{2^{4/2}} = (g^{r_1 + 16})^4 = g^{4r_1 + 64}$$

$$P \xrightarrow{v_2} V$$

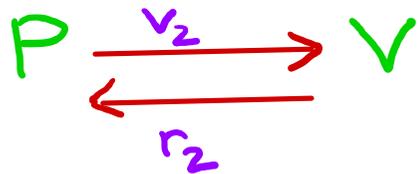
Pietrzak's VDF

Halving subprotocol for $T=4$:

$$g_1 = g^{r_1 + 16}$$

$$h_1 = g^{16r_1 + 256}$$

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Pietrzak's VDF

Halving subprotocol for $T=4$:

$$g_1 = g^{r_1 + 16}$$

$$h_1 = g^{16r_1 + 256}$$

$$v_2 = g_1^{2^{4/2}} = (g^{r_1 + 16})^4 = g^{4r_1 + 64}$$

$$P \xrightarrow{v_2} V \quad g_2 = g_1^{r_2} v_2$$
$$\xleftarrow{r_2}$$

Pietrzak's VDF

Halving subprotocol for $T=4$:

$$g_1 = g^{r_1 + 16}$$

$$h_1 = g^{16r_1 + 256}$$

$$v_2 = g_1^{2^{4/2}} = (g^{r_1 + 16})^4 = g^{4r_1 + 64}$$

$P \xrightarrow{v_2} V$
 $\xleftarrow{r_2}$

$$g_2 = g_1^{r_2} v_2 = g^{r_1 r_2 + 16r_2} \cdot g^{4r_1 + 64}$$
$$= g^{4r_1 + r_1 r_2 + 16r_2 + 64}$$

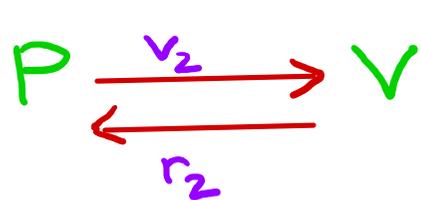
Pietrzak's VDF

Halving subprotocol for $T=4$:

$$g_1 = g^{r_1 + 16}$$

$$h_1 = g^{16r_1 + 256}$$

$$v_2 = g_1^{2^{4/2}} = (g^{r_1 + 16})^4 = g^{4r_1 + 64}$$



$$\begin{aligned} g_2 &= g_1^{r_2} v_2 = g^{r_1 r_2 + 16r_2} \cdot g^{4r_1 + 64} \\ &= g^{4r_1 + r_1 r_2 + 16r_2 + 64} \end{aligned}$$

$$h_2 = v_2^{r_2} h_1$$

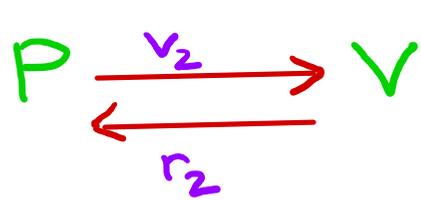
Pietrzak's VDF

Halving subprotocol for $T=4$:

$$g_1 = g^{r_1 + 16}$$

$$h_1 = g^{16r_1 + 256}$$

$$v_2 = g_1^{2^{4/2}} = (g^{r_1 + 16})^4 = g^{4r_1 + 64}$$



$$\begin{aligned} g_2 &= g_1^{r_2} v_2 = g^{r_1 r_2 + 16r_2} \cdot g^{4r_1 + 64} \\ &= g^{4r_1 + r_1 r_2 + 16r_2 + 64} \end{aligned}$$

$$\begin{aligned} h_2 &= v_2^{r_2} h_1 = g^{4r_1 r_2 + 64r_2} \cdot g^{16r_1 + 256} \\ &= g^{16r_1 + 4r_1 r_2 + 64r_2 + 256} \end{aligned}$$

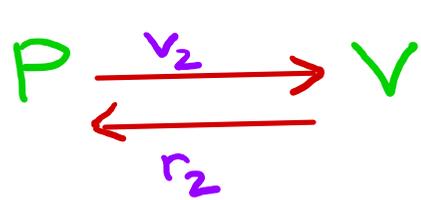
Pietrzak's VDF

Halving subprotocol for $T=4$:

$$g_1 = g^{r_1 + 16}$$

$$h_1 = g^{16r_1 + 256}$$

$$v_2 = g_1^{2^{4/2}} = (g^{r_1 + 16})^4 = g^{4r_1 + 64}$$



$$g_2 = g_1^{r_2} v_2 = g^{r_1 r_2 + 16r_2} \cdot g^{4r_1 + 64}$$

$$= g^{4r_1 + r_1 r_2 + 16r_2 + 64}$$

$$h_2 = v_2^{r_2} h_1 = g^{4r_1 r_2 + 64r_2} \cdot g^{16r_1 + 256}$$
$$= g^{16r_1 + 4r_1 r_2 + 64r_2 + 256}$$

input for next recursive layer:

$$T=2, g_2, h_2$$

Pietrzak's VDF

Halving subprotocol for $T=2$:

$$g_2 = g^{4r_1 + r_1 r_2 + 16r_2 + 64}$$

$$h_2 = g^{16r_1 + 4r_1 r_2 + 64r_2 + 256}$$

$$v_3 = g_2^{2/2}$$

Pietrzak's VDF

Halving subprotocol for $T=2$:

$$g_2 = g^{4r_1 + r_1 r_2 + 16r_2 + 64}$$

$$h_2 = g^{16r_1 + 4r_1 r_2 + 64r_2 + 256}$$

$$v_3 = g_2^{2/2} = g^{8r_1 + 2r_1 r_2 + 32r_2 + 128}$$

$$P \xrightarrow{v_3} V$$

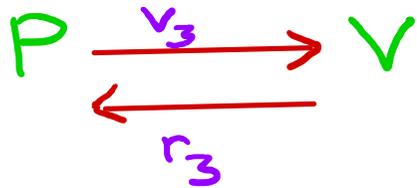
Pietrzak's VDF

Halving subprotocol for $T=2$:

$$g_2 = g^{4r_1 + r_1 r_2 + 16r_2 + 64}$$

$$h_2 = g^{16r_1 + 4r_1 r_2 + 64r_2 + 256}$$

$$v_3 = g_2^{2^{1/2}} = g^{8r_1 + 2r_1 r_2 + 32r_2 + 128}$$



$$g_3 = g_2^{r_3} v_3$$

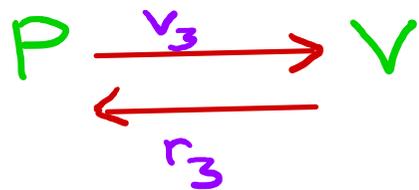
Pietrzak's VDF

Halving subprotocol for $T=2$:

$$g_2 = g^{4r_1 + r_1 r_2 + 16r_2 + 64}$$

$$h_2 = g^{16r_1 + 4r_1 r_2 + 64r_2 + 256}$$

$$v_3 = g_2^{2^{1/2}} = g^{8r_1 + 2r_1 r_2 + 32r_2 + 128}$$



$$\begin{aligned} g_3 &= g_2^{r_3} v_3 = g^{4r_1 r_3 + r_1 r_2 r_3 + 16r_2 r_3 + 64r_3} \\ &\quad g^{8r_1 + 2r_1 r_2 + 32r_2 + 128} \\ &= g^{8r_1 + 32r_2 + 64r_3 + 2r_1 r_2 + 4r_1 r_3 + 16r_2 r_3 + r_1 r_2 r_3 + 128} \end{aligned}$$

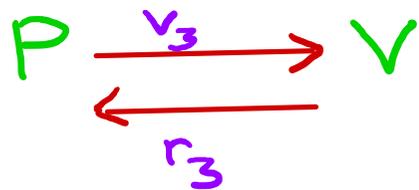
Pietrzak's VDF

Halving subprotocol for $T=2$:

$$g_2 = g^{4r_1 + r_1 r_2 + 16r_2 + 64}$$

$$h_2 = g^{16r_1 + 4r_1 r_2 + 64r_2 + 256}$$

$$v_3 = g_2^{2^{1/2}} = g^{8r_1 + 2r_1 r_2 + 32r_2 + 128}$$



$$g_3 = g_2^{r_3} v_3 = g^{4r_1 r_3 + r_1 r_2 r_3 + 16r_2 r_3 + 64r_3}$$

$$= g^{8r_1 + 2r_1 r_2 + 32r_2 + 128} \\ = g^{8r_1 + 32r_2 + 64r_3 + 2r_1 r_2 + 4r_1 r_3 + 16r_2 r_3 + r_1 r_2 r_3 + 128}$$

$$h_3 = v_3^{r_3} h_2$$

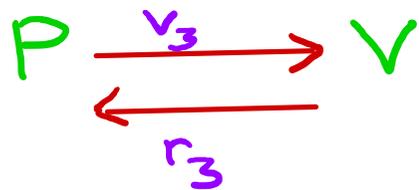
Pietrzak's VDF

Halving subprotocol for $T=2$:

$$g_2 = g^{4r_1 + r_1 r_2 + 16r_2 + 64}$$

$$h_2 = g^{16r_1 + 4r_1 r_2 + 64r_2 + 256}$$

$$v_3 = g_2^{2^{1/2}} = g^{8r_1 + 2r_1 r_2 + 32r_2 + 128}$$



$$g_3 = g_2^{r_3} v_3 = g^{4r_1 r_3 + r_1 r_2 r_3 + 16r_2 r_3 + 64r_3} \cdot g^{8r_1 + 2r_1 r_2 + 32r_2 + 128}$$

$$= g^{8r_1 + 32r_2 + 64r_3 + 2r_1 r_2 + 4r_1 r_3 + 16r_2 r_3 + r_1 r_2 r_3 + 128}$$

$$h_3 = v_3^{r_3} h_2 = g^{8r_1 r_3 + 2r_1 r_2 r_3 + 32r_2 r_3 + 128r_3} \cdot g^{16r_1 + 4r_1 r_2 + 64r_2 + 256}$$

$$= g^{16r_1 + 64r_2 + 128r_3 + 4r_1 r_2 + 8r_1 r_3 + 32r_2 r_3 + 2r_1 r_2 r_3 + 256}$$

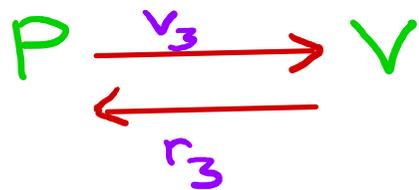
Pietrzak's VDF

Halving subprotocol for $T=2$:

$$g_2 = g^{4r_1 + r_1 r_2 + 16r_2 + 64} \quad \text{input for final recursive layer:}$$

$$h_2 = g^{16r_1 + 4r_1 r_2 + 64r_2 + 256} \quad T=1, g_3, h_3$$

$$v_3 = g_2^{2^{1/2}} = g^{8r_1 + 2r_1 r_2 + 32r_2 + 128}$$



$$g_3 = g^{r_3} v_3 = g^{4r_1 r_3 + r_1 r_2 r_3 + 16r_2 r_3 + 64r_3}$$

$$= g^{8r_1 + 2r_1 r_2 + 32r_2 + 128} g^{8r_1 r_3 + 32r_2 r_3 + 64r_3 + 2r_1 r_2 + 4r_1 r_3 + 16r_2 r_3 + r_1 r_2 r_3 + 128}$$

$$h_3 = v_3^{r_3} h_2 = g^{8r_1 r_3 + 2r_1 r_2 r_3 + 32r_2 r_3 + 128r_3}$$

$$= g^{16r_1 + 64r_2 + 128r_3 + 4r_1 r_2 + 8r_1 r_3 + 32r_2 r_3 + 2r_1 r_2 r_3 + 256}$$

Pietrzak's VDF

Halving subprotocol for $T=1$:

$$g_3 = g^{8r_1 + 32r_2 + 64r_3 + 2r_1r_2 + 4r_1r_3 + 16r_2r_3 + r_1r_2r_3 + 128}$$

$$h_3 = g^{16r_1 + 64r_2 + 128r_3 + 4r_1r_2 + 8r_1r_3 + 32r_2r_3 + 2r_1r_2r_3 + 256}$$

base case \rightarrow check $h_3 = (g_3)^2$

Pietrzak's VDF

Halving subprotocol for $T=1$:

$$g_3 = g^{8r_1 + 32r_2 + 64r_3 + 2r_1r_2 + 4r_1r_3 + 16r_2r_3 + r_1r_2r_3 + 128}$$

$$h_3 = g^{16r_1 + 64r_2 + 128r_3 + 4r_1r_2 + 8r_1r_3 + 32r_2r_3 + 2r_1r_2r_3 + 256}$$

base case \rightarrow check $h_3 = (g_3)^2$

Yes, indeed!

Pietrzak's VDF

Halving subprotocol for $T=1$:

$$g_3 = g^{8r_1 + 32r_2 + 64r_3 + 2r_1r_2 + 4r_1r_3 + 16r_2r_3 + r_1r_2r_3 + 128}$$

$$h_3 = g^{16r_1 + 64r_2 + 128r_3 + 4r_1r_2 + 8r_1r_3 + 32r_2r_3 + 2r_1r_2r_3 + 256}$$

base case \rightarrow check $h_3 = (g_3)^2$

Yes, indeed! V outputs 1

Pietrzak's VDF

Halving subprotocol for $T=1$:

$$g_3 = g^{8r_1 + 32r_2 + 64r_3 + 2r_1r_2 + 4r_1r_3 + 16r_2r_3 + r_1r_2r_3 + 128}$$

$$h_3 = g^{16r_1 + 64r_2 + 128r_3 + 4r_1r_2 + 8r_1r_3 + 32r_2r_3 + 2r_1r_2r_3 + 256}$$

base case \rightarrow check $h_3 = (g_3)^2$

Yes, indeed! V outputs 1

(takes $2\log_2 T$ exponentiations)

Pietrzak's VDF

Security:

Pietrzak's VDF

Security:

Can construct VDF assuming hardness of iterated squaring in \mathbb{G}

Pietrzak's VDF

Security:

Can construct VDF assuming hardness of iterated squaring in \mathbb{G} and ideal hash fn. (ROM)

Pietrzak's VDF

Security:

Can construct VDF assuming hardness of iterated squaring in \mathbb{G} and ideal hash fn. (ROM)

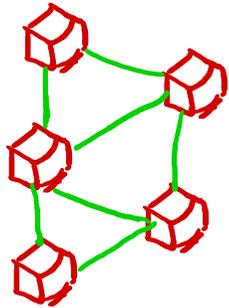
Applications:

Pietrzak's VDF

Security:

Can construct VDF assuming hardness of iterated squaring in \mathbb{G} and ideal hash fn. (ROM)

Applications:

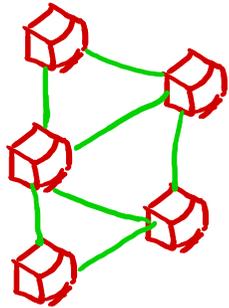


Pietrzak's VDF

Security:

Can construct VDF assuming hardness of iterated squaring in \mathbb{G} and ideal hash fn. (ROM)

Applications:



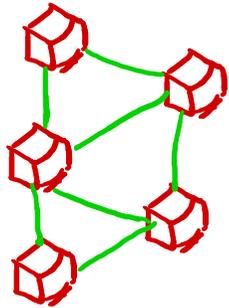
Chia network

Pietrzak's VDF

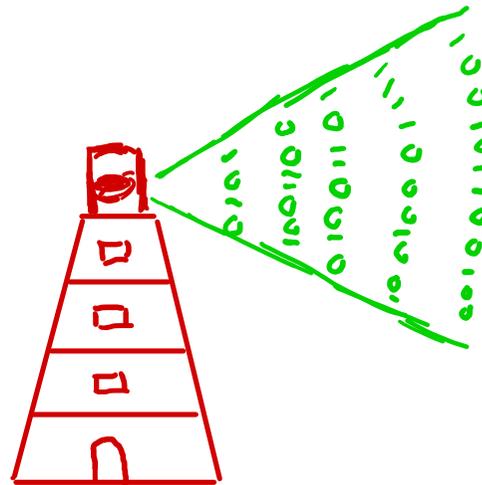
Security:

Can construct VDF **assuming** hardness of iterated squaring in \mathbb{G} and ideal hash fn. (**ROM**)

Applications:



Chia network



RandRunner

Thank

You!

References

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